## A MICROPROCESSOR CONTROLLED AUTOMATIC DATA LOGGING SYSTEM (ADL)

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John David Casko

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## NAVAL POSTGRADUATE SCHOOL Monterey, California



# THESIS

A MICROPROCESSOR CONTROLLED AUTOMATIC DATA LOGGING SYSTEM (ADL)

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June 1977

Thesis Advisor

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The particular application under consideration is automatic data collection and angle-of-attack control of a subsonic wind-tunnel. Data are presented to demonstrate the data logging capabilites of the system.

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A MICROPROCESSOR CONTROLLED AUTOMATIC DATA LOGGING SYSTEM (ADL)

by

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Submitted in partial fulfillment of the requirements for the degree of

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from the

NAVAL POSTGRADUATE SCHOOL

June 1977



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## ABSTRACT

This paper describes a digital, microprocessor controlled data acquisition system which optimizes man/machine communications. The processor provides digital feedback control, data collection over any number of channels (up to 8), 32 BIT floating point (7 significant digit) mathematics, and a variety of output formats. The main features of the device are the ability to work directly in any numerical unit desired by the user, mathematical noise filtering and automatic feedback control.

The particular application under consideration is automatic data collection and angle-of-attack control of a subsonic wind-tunnel. Data are presented to demonstrate the data logging capabilities of the system.

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### I. <u>INTRODUCTION</u>

#### A. BACK GROUND

The use of microprocessors (U-P) to control various analog and digital devices has grown exponentially in the past two years. Applications range from TV tennis games and 'smart' traffic lights, to industrial plant monitors and high speed data handling. The state-of-the-art U-P at the time of this writing is the INTEL 8748. This single integrated circuit contains:

- 1. Central Processing Unit (CPU)
- 1K bytes of erasable, programmable, read-only memory (EPRCM)
- 3. 64 bytes of random access memory (RAM)
- 4. 8-BIT interval timer/event counter
- 5. clock driver

In addition, this device draws only 150 milliampere (mA) at 5 volts (V).

Microprocessors have greatly enhanced the important technological application called DISTRIBUTED INTELLIGENCE. For example, routine - but time consuming - chores such as parallel to serial data conversion, x-y plotting, equipment polling, etc. can be controlled on site. Engineering analysis of such large interconnected subsystems reduces to a 'black box' problem rather than the more complex problem

B. DISCUSSION

This paper describes the development and construction of an automatic data logging system (ADL) which is configured via software to suit a particular application. The software modification is dynamic in nature, which means that the system operator needs only to type in a few simple commands to change the system input/output (I/O) to measure volts, feet, psi or any other quantity directly without external hardware modification or adjustment. The requirements for system and an overall description of the ADL hardware the are given in chapter II. Chapter III discusses the command words available along with examples of actual output. Chapter IV presents a specific application of the system. Guidelines for interfacing the digital feedback control function with various types of equipment are also given. Chapter V contains the software assembly listing as well as flowcharts and explanations of the more important routines. Chapter VI discusses the use of U-P development systems and gives recommendations for software development. Appendix A is a glossary of U-P and data acquisition terminology which is used throughout the paper.

## II. SYSTEM DESCRIPTION

#### A. REQUIREMENTS

The purposes of a data logging system are twofold. First, the system must be able to take readings from a variety of physical devices. Second, these readings must be converted into a form suitable for data reduction and human interpretation. The obvious use of such a system is taking data over extremely long or extremely short time periods, filtering out noise, controlling external events and providing tabular and/or graphical output. With the above in mind, the following requirements are defined:

- 1. 8 channels of analog input
- 1 channel for digital feedback control of some external device.
- 3. Plain language man/machine interface via serial data transmission.
- 4. Manual and automatic data acquisition functions.
- 5. Limited data manipulation.
- 6. Multiplexed digital voltmeter function.
- 7. Limited text file storage and editing.

It should be pointed out that the above requirements were defined with the wind-tunnel control function in mind (ch. IV). Nevertheless, the concepts may be extended to other applications with minor software modifications.



#### B. DEVICE SELECTION

A strictly hardware-oriented implementation of the system requirements was not a valid alternative due to the inherent inflexibility of such designs. Large scale computer installation was prohibitive from cost and under-utilization considerations. It was therefore decided to use an available microprocessor - the INTEL 8008 - to implement all logic and data manipulation functions. This U-P device is the heart of the PROLOG Corporation 805 microprocessor system. Figure 1 is a schematic of the 805 system layout as modified for this project. Appendix B presents vendor specifications for same. Figure 2 shows the overall system layout including the command and communications links between the system components and the operator.

#### C. INPUT/OUTPUT DEVICES

The man/machine interface was the most difficult task to implement. The major difficulty was not in the physical interface, but the language used for two-way communications. A software driven ASR-33 Teletype was used for command entry, data presentation, and test functions. Although teletype driving wastes CPU time, the time delays involved are still much less than the mechanical time delays of the relays and driving motors which the U-P is controlling.

A group of eight HEWLETT PACKARD 5082-7302 display lights was used to implement the digital voltmeter function. This display is used to set amplifier gains, set nulls and to verify that data present on a particular input are being

processed by the system. The light display is controlled via software to display data in volts.

Up to eight channels of analog data may be multiplexed (DATEL MM-8) into the sample-and-hold unit (DATEL SHM-4), as shown in figure 3. The analog-to-digital (A/D) converter (DATEL ADC-149) has 14-BIT resolution over a 20-volt range. These three devices are also driven via software in order to provide various time delays between data samples. The time delays are utilized to mathematically filter out low level noise and A/D glitches from the system. Appendix B contains vendor data for the above mentioned devices.

#### D. FUNCTIONAL ARCHITECTURE

## 1. <u>General</u>

The primary advantage of a software configured system is that its processing functions and I/O can be modified wihout external hardware adjustment. This implies that the system possesses a 'general purpose' quality. However, a compromise must be made between a completely general system and one which can be easily implemented by an operator who has little knowledge of the operating system (OS) software or of the dynamics of the system from which this person is collecting data. In order for the data logging system to be used effectively by students in a variety of engineering disciplines, the OS was set up to optimize man/machine interaction. Thus, the operator has no control over such parameters as relay and drive motor transportation lag. These particular system parameters are fixed (see chapter IV) but still provide wide applications such as probe placement and angle setting. Although obvious,



it is worth mentioning that the feedback controlled movement of an external device may not be coincident with data acquisition and reduction, as the U-P can perform only one function at a time. In general, the internal data handling of the microprocessor is transparent to the user.

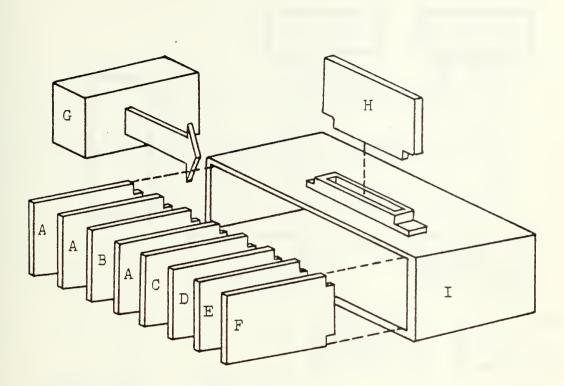
## 2. Internal

Figure 4 is a flowchart of the basic numerical data conversion processes. Note that two levels of conversion take place. The first level converts data from the 14-BIT provided by the A/D converter into a numerical binary voltage between -10 and +10 volts. This interpolation routine is called before any raw data are processed. The next level of conversion is accomplished with a scaling routine which changes voltage units into any unit desired by the user. If the user does not specify a particular scaling factor, the system defaults to volts for all I/O presentations. Scaling factors can be changed at any time, on any of the input channels; different channels may have different scaling factors.

The mathematics package used is from the INTEL Users Library and is discussed in Appendix C. Although the math package performs all operations with 7 significant digits, numerical output is rounded to 4 significant digits ( with choice of decimal or exponential notation ). This was done to improve readability of tabulated output and to permit all eight channels to be printed in the limited space provided by the teletype. The only exception is the DUMP routine (ch. III), which always outputs 7 digits. This is because scaling factors of up to 7 digits can be entered by the operator (also fig. 4).

The decimal format presents data between 0.0001 and

9999. The exponential format presents 4 significant digits between 1.000 E - 28 and 10.00 E + 27. All numerical entries by the operator can be in either format, with the exception of channel numbers, which are only single digit integers.



- A 2K EPROM
- B CPU
- C 4K RAM
- D Input ports
- E Output ports
- F Serial interface
- G Power supply
- H Sockets
- I Card cage

Modular construction of the U-P components enhances expansion.

Figure 1 - PROLOG 805 SYSTEM LAYOUT



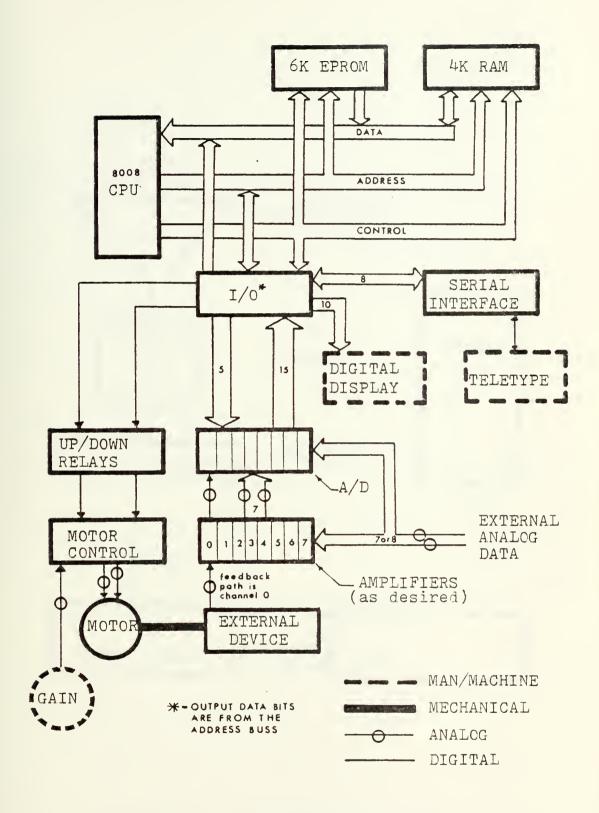
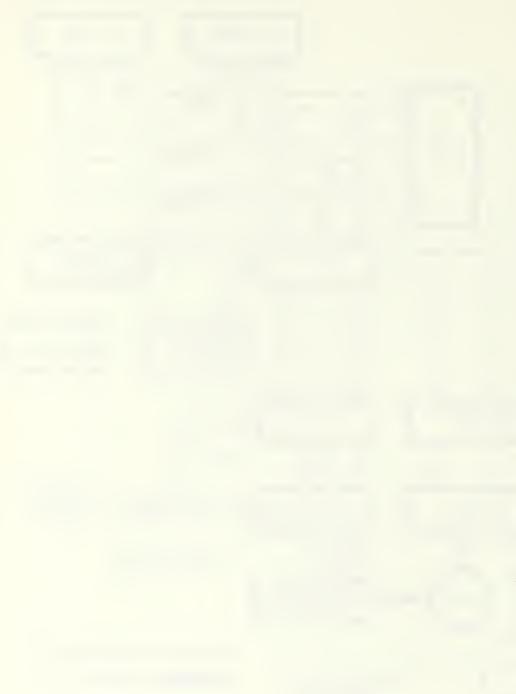


Figure 2 - ADL/805 INTERFACE



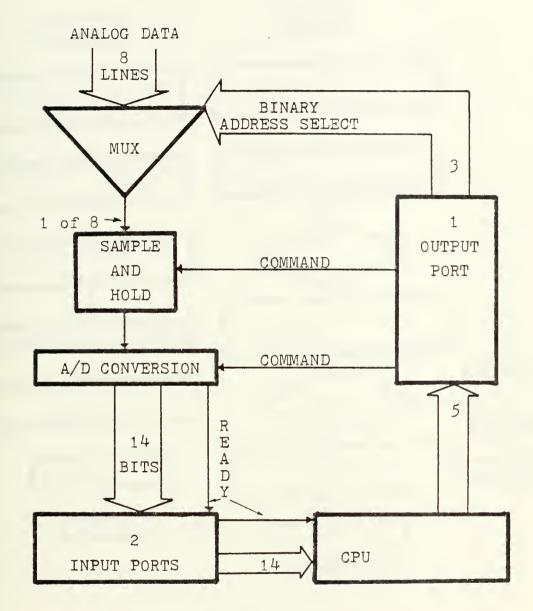


Figure 3 - A/D CONVERSION SYSTEM



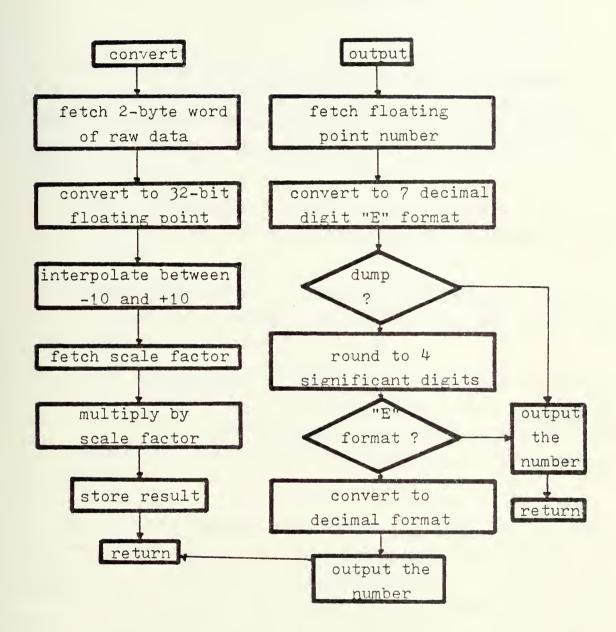


Figure 4 - NUMERICAL DATA CONVERSION METHODS



## III. COMMAND WORDS

This chapter defines the commands which the operator may use to communicate with the ADL. A brief explanation of the word is presented; then the rules for it's usage and the ADL response is given. The commands are organized into three categories:

- <u>DATA DEFINITION</u>: Used to set scaling and delay parameters, and to store the sequence of channels to be scanned.
- <u>ACQUISITION</u>: These commands start various types of acquisition processing, including the voltmeter and feedback control routines.
- 3. <u>FILE MAINTENANCE:</u> These are convenience features which provide simple text editing and printing of repetitious table headings. They also provide abort capability and correction capability for misspelled words.

Note that command words are entered in upper case just as they appear in the following paragraphs. When the ADL is ready for a command, it prompts with the symbol >. The command word is then entered followed by pressing the RETURN key. In all examples given, the ADL-generated text is shown in parentheses for illustration purposes only. Section D of this chapter presents copies of actual printout from typical runs in order to further clarify the use of the commands.

- - - -

## A. DATA DEFINITION

### 1. UNIT

This command is normally the first used. It enables the user to specify a scaling factor so that all data I/O will be in any desired unit. A typical sequence of commands could appear as follows:

```
(>) UNIT
(CHANNEL =) 1
(UNIT/VOLT) 10.5
(CHANNEL =) 6
(UNIT/VOLT) .2
(CHANNEL =)
(>)
```

Thus the user has specified a scaling factor of 10.5 for channel 1 and .2 for channel 6. The abort command was then used to terminate the routine. The ADL responded with > when again ready to accept commands. The result of the above is that, for example, a 3-volt input on all channels will print out as 3.000 on channels 0,2,3,4,5,7; as 31.50 on channel 1 and as .6000 on channel 6.

## 2. WAIT

this command is used to set a known time delay between sets of data points. The ADL takes 128 data points from each channel and computes an average before a value is printed. Thus, the effects of noise and A/D glitches are

minimized. Because it takes 1 millisecond (ms) for the ADL to set up the multiplexer for a different channel, the WAIT delay must be used with caution in an environment which exhibits periodic noise. If only one channel is scanned, the time between data points will be as entered. However, if all 8 channels are scanned the time between data points on any channel will be the desired time plus 8 ms (1 ms for each channel).

## 3. <u>SET SCAN</u>

SET SCAN is a dual purpose routine. First of all, if a WAIT command has not been issued it will default to 15 ms and send a message to the operator; otherwise, it proceeds to the next step. The operator is then given the opportunity to specify the type of output format, namely, exponential or decimal. Second, the ADL asks the operator to input the channels to be scanned in the desired sequence. Section D presents some typical examples of SET SCAN usage.

#### B. ACQUISITION COMMANDS

### 1. <u>READ</u>

This is the command used to start the voltmeter function. A typical entry would look like:

(CHANNEL =) 3

When the RETURN key is pressed, the digital display will follow the data on channel 3 (in volts). The display is updated every 10 ms and takes the data through a 43 microsecond window. When the display is in operation, any

noise on the input will show up as a rapidly changing digit. This is unlike most digital voltmeters which integrate the input over a small time interval and present an average reading. This integration process may effectively mask any noise down to fairly low frequencies, depending on the voltmeter being used.

The main use of the above function is to set gain limits on the inputs. To transfer command back to the user, any key on the teletype can be pressed. The ADL responds with > and is then ready for another command.

## 2. <u>SCAN</u>

SCAN is used to manually control the tabulation of Upon command entry, the ADL checks to see if channel data. assignments have been made via the SET SCAN routine. If the check is negative, a message is sent to the operator and the routine is aborted. If the check is positive, headings are printed out with the proper spacing for the desired numerical format. The ADL then waits for a RETURN at which time a set of data is taken, averaged and printed out with the proper scaling factor applied. The printer carriage is then positioned at the end of the line of data so the user may enter any comments. The next set of data is taken when the RETURN key is pressed. Before each line of data is printed, a three digit counter, called the coordination number, is incremented automatically. The SET SCAN routine is used to reset this counter to zero (also automatic). Thus, repeated calls to SCAN or RUN will keep the counter indexing properly. The SCAN routine is terminated by entering the abort command.

## 3. MOVE

-

Channel 0 was internally defined as the feedback channel for ADL control of some external device. The operator inputs the desired position (speed, angle, etc.) and the ADL will provide the logic necessary to drive the device to within 4 A/D counts; 1 A/D count is equal to 1.22 millivolt (mV). A sample is taken every 0.8 ms so the maximum slew rate at the input is limited to 6.1 mV/sec in order to guaranty convergence. This routine is used mainly to ensure slew rates are not excessive and that external device movement does not exceed acceptable limits. Chapter V presents a detailed flowchart of the feedback logic used in the ADL.

## 4. RUN

RUN internally calls the SCAN and MOVE routines in repetition in order to automate the tabulation of data at many different positions of an external device. A typical data entry sequence for RUN could be:

(START POSIT =) 10.0 (STOP POSIT =) 7.5 (INCREMENT =) .5

Note that the start position does not need to be less than the stop position. Also note that the incremental movement is in absolute value. Upon execution (the RETURN key) the ADL prints out column headings as defined by the SET SCAN routine, slews the external device to the start position and starts tabulating data at each of the positions between START and STOP. When the stop position data have been printed out, command is returned to the operator.

#### C. FILE MAINTENANCE

The following commands are used to input text information and comments.

#### 1. EDIT

When the same heading information will appear as part of the documentation of each run, EDIT is used to enter this information for later use. After the desired text has been entered, the LINE FEED (L/F) key is pressed, followed by the RETURN key. The L/F is needed internally to mark the end of the file; this is the only routine that terminates with other than just the RETURN key. If the L/F key were not used, the entire buffer space (256 characters) would print out. At this time, if the END-OF-FILE symbol were not detected, an error message would be sent to the operator.

The EDIT mode can also be used to change or correct the text at any time. The routine is entered via the command word; the CONTROL and Z keys (cntrl-Z) are pressed simultaneously to step through the file. When the proper place in the text is reached, the RUBOUT key is pressed, then the new character is entered. Note that this entry is not inserted between two characters, but rather it overwrites; any number of characters can be reentered. In order to exit the routine without using the L/F key (which would truncate the text), the cntrl-A keys are used. Section D shows the construction and edit of a file.

# 2. FILE



The text entered with the EDIT command is printed out upon execution of FILE. Two lines are skipped automatically at the beginning and at the end of the file.

## 3. <u>DUMP</u>

Execution of DUMP will cause the contents of the conversion factor buffer to be printed on the teletype. This enables the user to verify the scaling factors which are being applied to each channel. Numbers are printed with 7 significant digits.

#### 4. MIEST

This command is executed automatically upon system reset or when power is applied. All the ram area is tested by first writing OOH to each location, reading it back and comparing the result with OOH; the same process is repeated with FFH (Appendix C contains an explanation of hexadecimal notation). If an error were detected, the contents of the bad location would be printed out along with its address. This enables the operator to identify the particular circuit component which has malfunctioned. The routine can also be entered as a command, but use of this function resets all default values just as a system reset.

## 5. <u>CNTRL-A</u>

The abort command is used to terminate execution of all routines except RUN and READ. When the cntrl-A keys are used, command is transferred back to the operator and the system responds with a >. The RUN routine can only be

terminated with a system reset; the READ routine is terminated by pressing any of the teletype keys.

## 6. <u>CNTRL-R</u>

Pressing these two keys causes the phrase RUN NO. to be printed. The keyboard is then opened for the insertion of any desired alpha/numeric single line sequence.

# 7. <u>CNTRL-C</u>

This command causes \*\*\*\* to be printed in order to flag a comment. Note that this is a command routine and can only be entered after a > prompt by the ADL.

## 8. <u>CNTRL-Z</u>

Pressing these keys causes an internal counter to advance forward through the input buffer. In this manner, the contents of memory can be displayed (see EDIT).

### 9. RUBOUT

This key is used mainly to correct spelling errors or to correct data entries without aborting a routine. For example, assume the operator wants to enter SCAN but notices that SVAN has been typed by mistake. The RUBOUT key is pressed three times. Each time it is pressed, the previously entered character is printed and an internal counter counts backwards through the input buffer. The operator then retypes the correct letters (CAN) and the correction is complete. The teletype entries would then look like:

- - - -

#### SVANNAVCAN

When executed the ADL will only see SCAN.

Command word recognition is accomplished by summing the binary codes of each of the letters of the word. The result is then compared with a list of valid sums which are contained in memory (check-sum). Since the result of a summation does not depend on the order of the addends, the letters of the command word may be entered in any order (e.g., SCAN, NACS, etc.). Although this method could lead to ambiguity problems, the vocabulary of the ADL is small enough to prevent such an inconsistency. Any command ,text or data entry can be edited with the RUBOUT key at any time.

D. EXAMPLES

The figures following this section are copies of actual ADL sessions. All ADL-generated messages are underlined the first time they appear for illustration purposes only.

Figure 5 shows a DATA DEFINITION sequence. The UNIT command was used to override the volt default on channels 0,1 and 2. The SET SCAN routine was used to select decimal format and to sequence channels 6,7,0,3,6. Note that the channels do not need to be in any particular sequence and that one channel may be used more than once. The use of a channel more than once enables the user to check the effectiveness of the noise filtering algorithm in a particular application. In this case, data taken on the first channel 6 scan will be 19 ms out of phase with the second channel 6 scan (15 ms for the delay parameter and a 1 ms intercycle delay for each channel). The operator then used the wait routine to change the wait parameter to 3 ms.

The subsequent call to the SET SCAN routine did not produce the default message (labeled A on the previous call).

Figure 6 shows a typical SCAN sequence which resulted from the commands entered from fig. 5. SCAN was used to take 5 sets of data, then RUN was used to take 5 sets. It is important to note that channel 0 must be included in the SET SCAN definition before RUN is executed. This is because channel 0 is the feedback path for the digital control functions.

Figure 7 shows the ADL response to improper inputs. After a reset, the operator tried to execute SCAN without first defining the channel sequence. The next example in this figure shows an invalid command followed by some examples of using the RUBOUT key to correct various entries.

In fig. 8 the operator did not use a LINE FEED/RETURN sequence to mark the end of the text. The resulting call to FILE is then shown.

Figures 9 and 10 present the data from a wind tunnel calibration session. Figure 11 is a graph of the lift data (channel 2) versus angle-of-attack (AOA, channel 0). Notice that the scaling factors for channels 0 and 2 were selected so that their respective output would be read in degrees and pounds directly. Figure 12 shows a run which utilized exponential format.

```
*** RESET: ALL CHANNEL I/O IN "VOLTS" ***

\geq UNIT

<u>CHANNEL</u> = 0

<u>INIT/VOLT</u> = \cdot 1

CHANNEL = 1

UNIT/VOLT = 500.

CHANNEL = 2

UNIT/VOLT = 144

CHANNEL =
```

A > SET SCAN
 DELAY BETWEEN DATA POINTS = 15 MS (DEFAULT)
 "E" FORMAT(Y OR N) ? N
 INPUT CHANNELS IN DESIRED ORDER
 6.7. 0 3 6
 WHEN READY TO TAKE DATA, TYPE SCAN OF RUN

```
> WAIT

VALID FACTORS:

A = 3MS

B = 15MS

C = 25MS

A
```

> SET SCAN "E" FORMAT(Y OR N) ? N INPUT CHANNELS IN DESIRED ORDER 0,1,2,3,4,5

WHEN READY TO TAKE DATA, TYPE "SCAN" OF RUN

Figure 5 - DATA DEFINITION EXAMPLES



.

>	SCAN	
---	------	--

	CH. O	CH• 1	CH• 2	CH• 3	CH• 4	CH• 5
001	• 0997	-25.98	-1.308	-•0574	•0355	• 0006
002	• 0997	-25.95	-1.306	-•0574	•0357	•0006
003	• 0997	-26.05	-1.334	0572	•0366	• 0006
004	• 1118	-25.87	-1.276	-•0641	.0424	• 0006
00 5	• 1119	-25.89	-1.163	-•0639	• 0426	• 0006

1								
POSIT	= • 1							
STOP POSIT =02								
EMENT =	• 03							
CH. O	CH • 1	CH• 2	CH• 3	CH• 4	CH• 5			
• 0995	-25.86	-1.261	-•0568	•0371	•0006			
• 0 6 9 8	-25•56	-106.0	-•3309	-•1288	• 0006			
• 0398	-25.56	80.72	• 1612	·0127	• 0006			
• 0097	-25.41	27.19	-•0509	0454	•0005			
0202	-25.32	-22.10	• 0410	• 0162	• 0006			
	POSIT POSIT POSIT CH. 0 .0995 .0698 .0398 .0097	POSIT = .1 POSIT =02 <u>MENT =</u> .03 CH. 0 CH. 1 .0995 -25.86 .0698 -25.56 .0398 -25.56 .0097 -25.41	POSIT =       .1         POSIT =      02         PMENT =       .03         CH. 0       CH. 1       CH. 2         .0995 -25.86       -1.261         .0698 -25.56       -106.0         .0398 -25.56       80.72         .0097 -25.41       27.19	$\begin{array}{rrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrr$	$\begin{array}{rrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrr$			

>

Figure 6 - SCAN AND RUN EXAMPLES

\*\*\* RESET: ALL CHANNEL I/O IN "VOLTS" \*\*\* > SCAN 01: CHANNELS NOT DEFINED > SET SXAAXCAN DELAY BETWEEN DATA POINTS = 15 MS (DEFAULT) "E" FORMAT(Y OR N) ? N INPUT CHANNELS IN DESIRED ORDER 0 WHEN READY TO TAKE DATA, TYPE SCAN OF RUN > RUN START POSIT =  $150051 \cdot 150$ STOP POSIT =  $\cdot 05$ INCREMENT = .05 CH. O # • 1514 001 .0984 002 003 • 0492

>

.

.

Figure 7 - IMPROPER INPUT EXAMPLE

> EDIT THIS COMMAND IS USED TO INPUT TEXT WHICH IS USED MANY TIMES DURING A DATA LOGGING SESSION. THE "FILE" COMMAND IS USED TO RECALL THE TEXT. > FILE THIS COMMAND IS USED TO INPUT TEXT WHICH IS USED MANY TIMES DURING A DATA LOGGING SESSION. THE "FILE" COMMAND IS USED TO RECALL THE TEXT. > FILE THIS COMMAND IS USED TO INPUT TEXT WHICH IS USED MANY TIMES DURING A DATA LOGGING SESSION. THE "FILE" COMMAND IS USED TO RECALL THE TEXT. > EDIT THE RUBOITTIUT IS USDGTYYTGDEFUL > FILE THE RUBOUT IS USEFUL > 8 \*\*\* RESET: ALL CHANNEL I/O IN "VOLTS" \*\*\* > EDIT > EDIT TTTTTT CNTRL-Z USED HERE > FILE TTTT CNTRL-Z USED HERETTTTTTT 02: INVALID "FILE" TERMINATION

Figure 8 - EDIT AND FILE EXAMPLES

```
- [IVI] <
 CHANNEL = 0
 ONEI/VOLT = 10
 CHAVVEL = 1
 UNIT/VOLT = +,, +2250+3
 C = A V V F L = 2
 iNIT/VOLT = -JE \cdot 154
 CHANVEL =
 > SET SCAV
 LELAY EFIWEEN DATA POINTS = 15 45 (LEFAULT)
 "E" F(EMAICY ON N) ? N
 INPUT CHANNELS IN DESIGEL OLDER
 012
WHEN HEALY TO TAKE DATA, TYPE SCAN ON HUN
****
       JIND OFF TAKES
> SCAN
# CH• O CH• 1 Cn• 2
    •0061 •0115 -•2714 WIND OFF ZERO
001
> EUV
START POSIT = -6
STOP POSIT = 14
INCREMENT = 1
    Сн. 0 Сн. 1 Сн. 2
#
002 -6.015 .0135 -.7822
             •0123 -•2274
003 -4.972
    -3.986 .0138 -.3127
-2.996 .0109 -.2283
004 -3.986
005
006 -2.000
             •0151 -•2036
    -.9803 .0154 -.2384
.0061 .0112 -.3301
1.031 .0153 -.2421
2.016 .0160 -.2613
007 -• 9803
800
600
010
•0221 -•1953
016
    8.013
             .0237 -.2366
017
     9.000
    10.02 .0239 -.2549
11.01 .0237 -.1678
018
019
020
     12.03
             •0285 -•1586
021
     13.03 .0210 -.0953
022 14.02 .0217 -.0586
```

Figure 9 - WIND-TUNNEL TARE DATA



LATA HUN AT U = 40 FSr 1: JUNE 1977 \*\* \* \* \* > SCAV # CH• 0 CH• 1 6.2 · UC61 • 0297 02E -.0506 .0295 .0061 -.0556 027 023 .0061 .0293 -.0551 > 100 START POSIT =  $-\epsilon$ = 14 STOP FOSIT INCHEMENT. = 1 Cri+ 0 Cri+ 1 5 • PJ ħ. -6.040 40.17 -27.05 680 030 -4.198 39.69 -23.60 031 -3.997 39.68 -13.24 -2.963 37.60 -13.61 032 033 -1.397 40.02 -8.854 034 -. 2773 39.79 - 4. 446 035 •0061 39.87 · 507 2 036 1.032 40.22 5.316 2.019 40.48 10.44 037 038 3.036 40.52 14.51 4.024 41.10 17.53 03₹ 040 5.041 40.61 24.65 041 ۥ008 40.53 29.07 042 7.025 41.05 34.11 40.23 37.71 043 5.013 41.27 40.17 044 1.003 40.71 45.30 045 10.02 04F 11.01 39.85 46.12 047 12.03 40.32 43.20 13.03 37.71 47.69 045 049 14.02 39.37 40.12 > SCAN Сп. 0 Сп. 1 Сп. 2 4 .0568 -.0566 JINE OFF 4ERU 050 .0061 > LUMM 10.00000 9.225013 -96.15401 1.000000 1.000000 1.000000 1.000000 1.000000 Figure 10 - WIND-TUNNEL DATA RUN

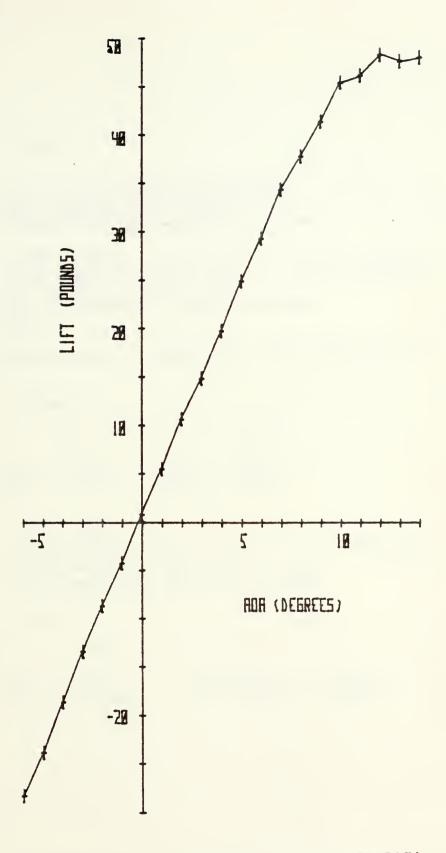


Figure 11 - PLOT OF WIND-TUNNEL DATA



THIS IS AN EXAMPLE OF DECIMAL AND EXPONENTIAL FORMAT. \*\*\*\*\*\*\*\*\*\*\* > SET SCAN DELAY BETWEEN DATA POINTS = 15 MS (DEFAULT) "E" FORMAT(Y OR N) ? N INPUT CHANNELS IN DESIRED ORDER 012 WHEN READY TO TAKE DATA, TYPE "SCAN" OR RUN > SCAN CH• 0 CH• 1 CH• 2 # 001 -5.036 -.0413 -.0019 002 -5.036 -.0413 -.0018 > > SET SCAN DELAY BETWEEN DATA POINTS = 15 MS (DEFAULT) "E" FORMAT(Y OR N) ? Y INPUT CHANNELS IN DESIRED ORDER > SCAN CH• 0 CH + 1 CH· 2 . -5.036 -4.160E-02 -3.110E-03 001 002 -5.035 -4.138E-02 -2.680E-03 >

> FILE

Figure 12 - EXPONENTIAL FORMAT EXAMPLES

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## IV. WIND TUNNEL APPLICATION

The ADL system was constructed in order to facilitate data acquisition and documentation from the 3.5 x 5.0 foot subsonic wind-tunnel located in the Department of Aeronautics at the Naval Postgraduate School. Logging data by hand from the tunnel balance is time consuming, error inducing and produces somewhat biased and scattered results. Other related problems are:

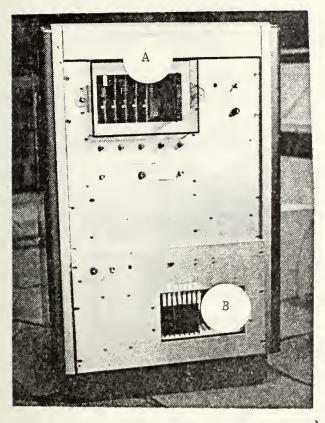
- 1. Personnel communications in a noisy environment.
- Meter reading while the quantity to be measured is subjected to random perturbations.
- 3. Tunnel heating due to long run times.
- 4. Time consuming AOA setting.

The ADL effectively eliminated all the above problems and in addition it proved to be versatile enough to be used as a data logger with other equipment. Figure 13 is a picture of the ADL installation. It fits compactly into a roll-around cabinet and requires only a standard 20 mA current loop, 110 baud I/O device (e.g., a teletype), and patch cords to connect it to the voltage sources it is to monitor. Five variable-gain, linear amplifier cards are included to provide low level signal buffering. Voltage sources can be connected to the ADL directly or patched through an amplifier, as long as the input excursions do not exceed -10V to +10V.

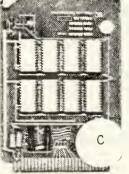
The feedback control function is implemented via two output lines - one labeled UP and the other labeled DOWN.

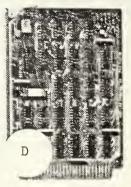


Each line carries an independent logic level voltage which is used to actuate a relay. The two relays in turn are used to control the direction of a motor. The desired feedback quantity (in this case position) is input to channel 0 which closes the digital control loop. Figure 14 is a detailed schematic of the AOA control as used in the wind tunnel system. To date, the ADL was used to calibrate the wind tunnel balance and the dynamic pressure transducer [1].



- A Amplifier cards and A/D modules
- B 805 Microprocessor





- C 2K PROM memory
- D CPU
- E A/D, sample-and-hold and multiplexer
- F Linear amplifier

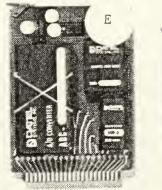




Figure 13 - PHOTOS OF ADL COMPONENTS



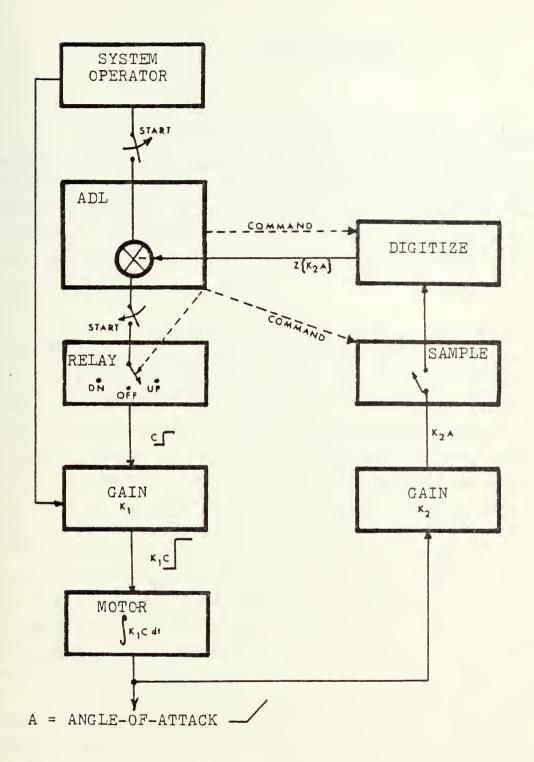


Figure 14 - ANGLE-OF-ATTACK FEEDBACK LOOP



## V. SOFTWARE DESIGN

The assembled program listing for the ADL is presented in this chapter along with flowcharts for the most important routines. Throughout the following paragraphs, frequent reference is made to the 'position' of an external device. 'Position' is used for illustration; speed, angle or many other attributes of the state of an external device can be used as a feedback parameter.

Figure 15 shows the averaging process used in the filter routine. A running sum is taken at 128 data points. This sum is then divided by 128, converted back to binary and stored as a two byte quantity in registers D and E. The binary representation of the current desired position of the device is recalled from memory and stored in external registers B and C. Upon exit from the routine, the registers up to compare the actual and desired positions in are set order to determine in which direction to move the external device. The 'UP' driver is next shown. It makes use of the previous subroutine to determine when the desired position has been reached. The position correction routine in fig. 17 determines if the position arrived at by the UP or DOWN routines has met predefined error criteria. There are also routines for the DOWN direction that are identical to figs. and 17 (except for the direction of movement). The MOVE 16 routine is in fig 18; it provides the logic necessary to properly call the UP and DOWN routines. Figures 19 and 20 are flowcharts of the RUN routine. RUN provides the automatic centrol function of the ADL by calling SCAN and MOVE at external device positions defined by the user.



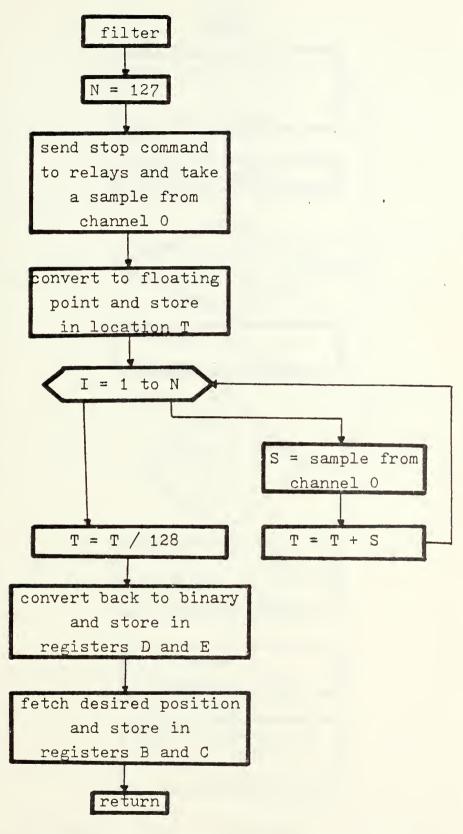


Figure 15 - NOISE AND GLITCH FILTER LOGIC



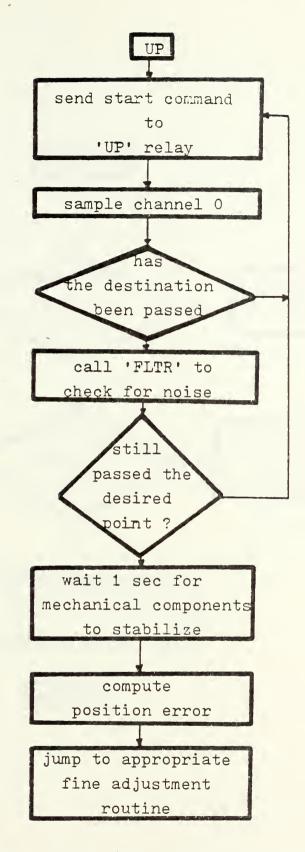


Figure 16 - 'UP' RELAY DRIVER LOGIC



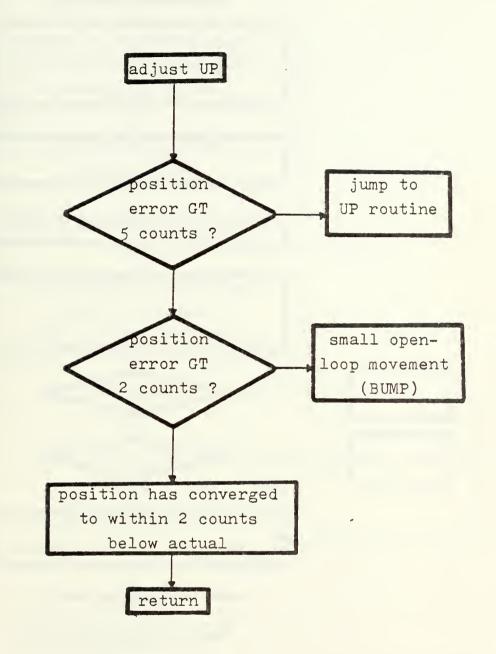


Figure 17 - OVERSHOOT/UNDERSHOOT CORRECTION LOGIC



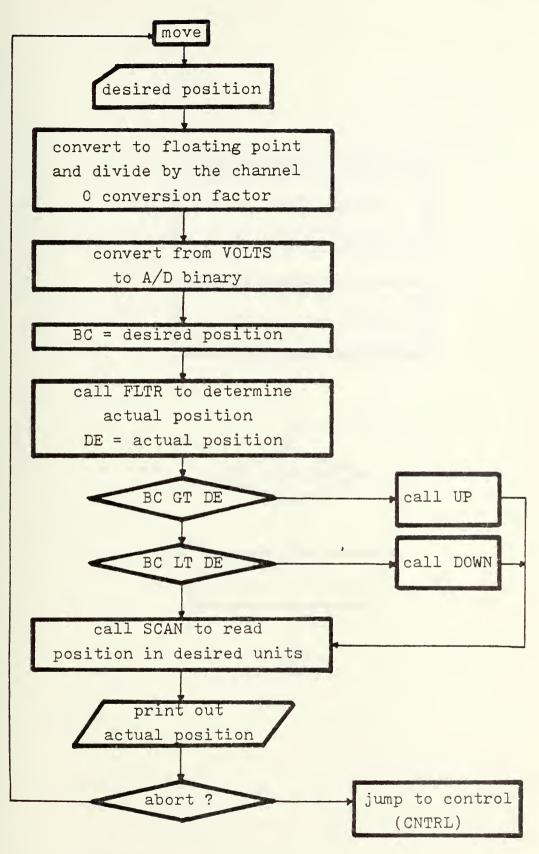
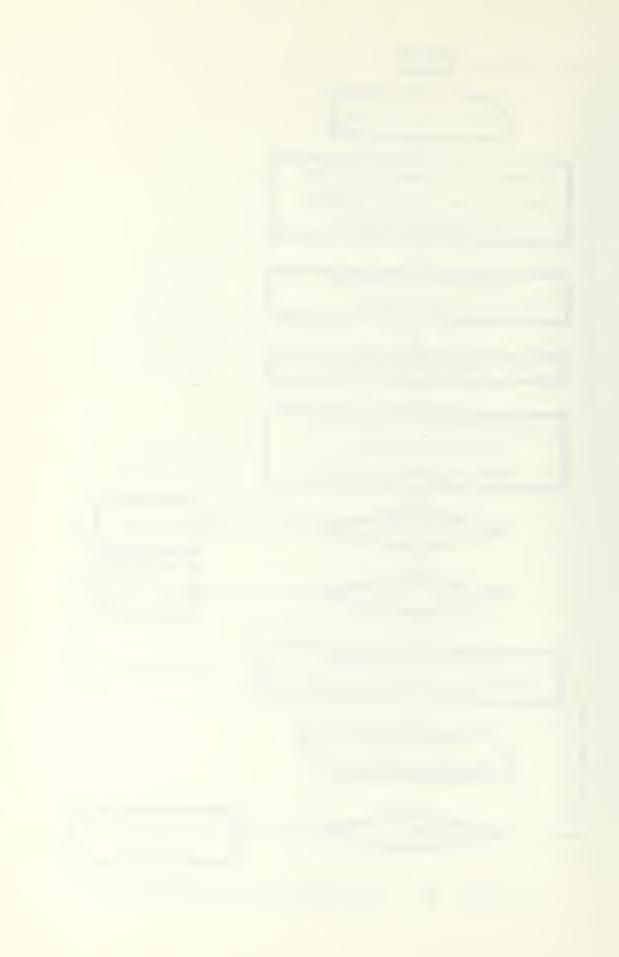


Figure 18 - EXTERNAL DEVICE CONTROL LOGIC



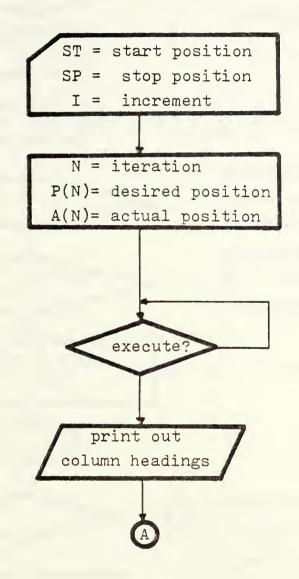


Figure 19 - RUN ROUTINE LOGIC (PART 1)



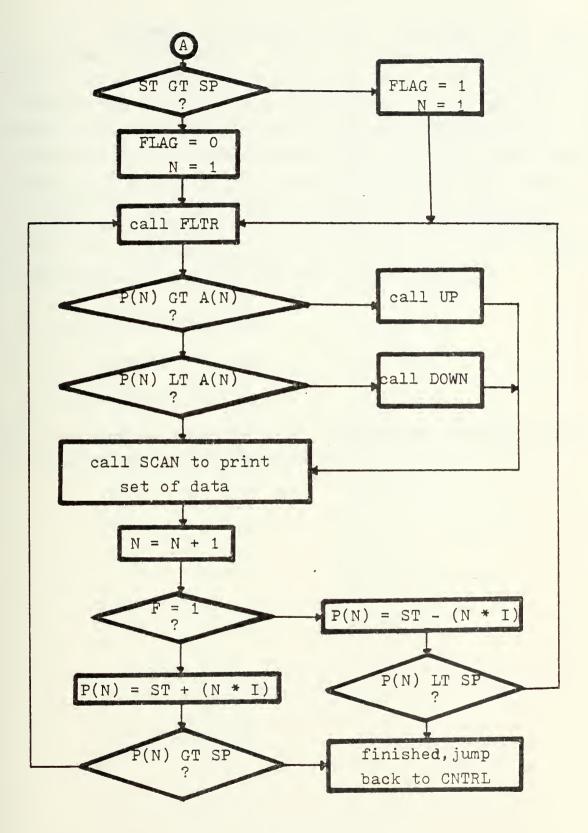
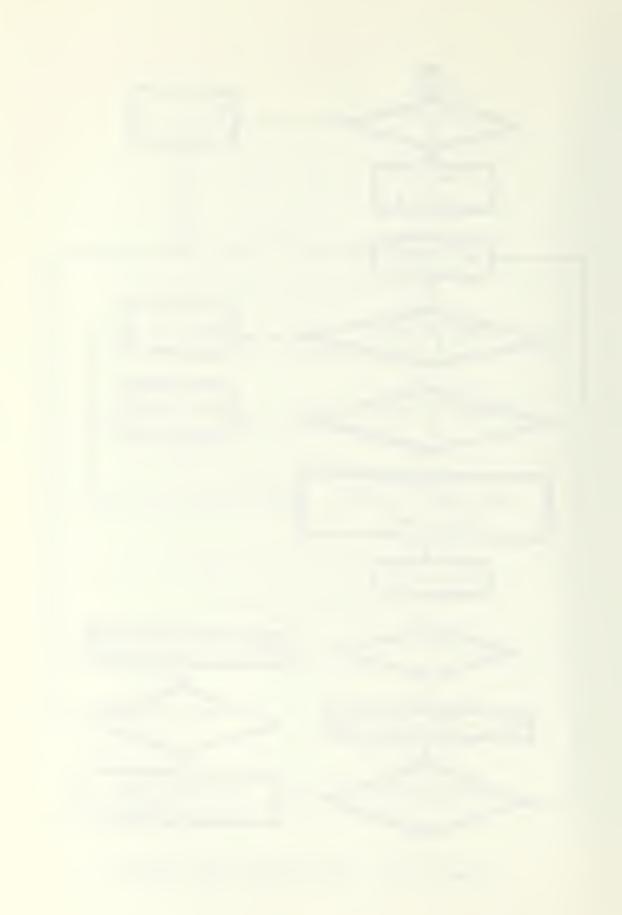


Figure 20 - RUN ROUTINE LOGIC (PART 2)

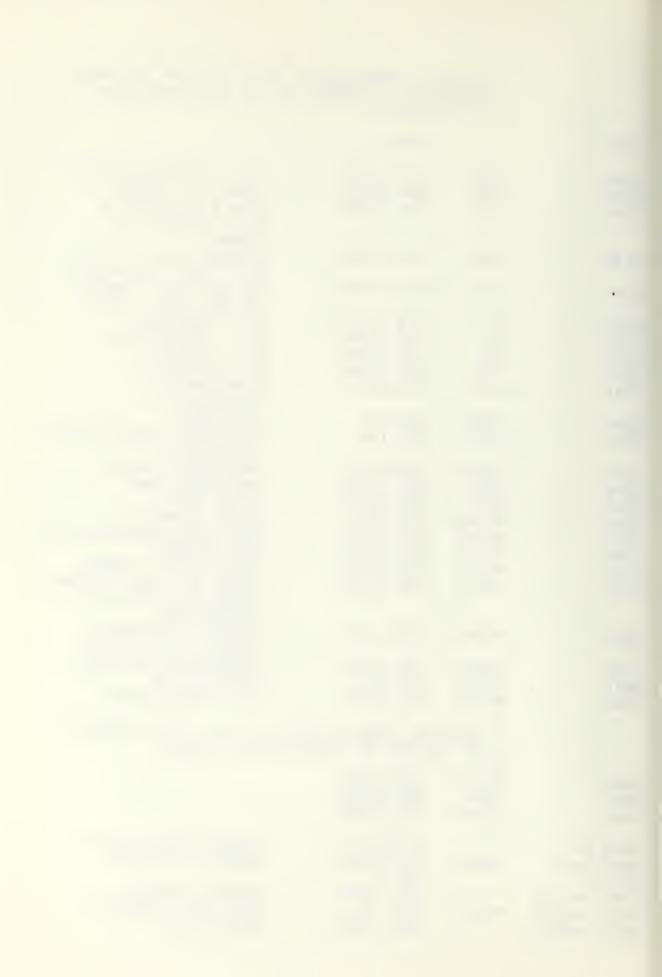


The following software was developed in small independent subroutines. This was done so that future revisions to logic could be accomplished with a minimum of redesign. For example, if a line printer is added to the system, all references to subroutine CO (console output) are changed so that the new routine can be called. The EQUATE tables at the start of each section of software are modified to reflect the address of the new routine, and the software is then reassembled. Similar procedures can be followed to alter I/O assignment, add new routines, etc. Memory requirements for the ADL software are as follows:

- 1. Pages OOH to OFH, ROM
- 2. Pages 20H to 27H, ROM
- 3. Pages 10H to 1FH, RAM

This program was compiled on the INTELLEC 8 microprocessor development system for the 8008 U-P.

	; SUBRC		THIS SECTION CONTAINS ALL MAJOR SECTIONS
0000	,	ORG O	
032F 036E 033E	INIT LOD STR	EQU 032FH EQU 036EH EQU 033EH	F.P. INITIALIZATION LOAD F.P. ACCUM PLACE CONTENTS OF F.P. ACCUM INTO
064B	INN	EQU 064BH	MEMORY CHANGE BCD DATA TO
070F	ouu	EQU 070FH	;F.P. AND LOAD ACCUM. ;CHANGE F.P. NUMBER
03 D7 03 D4 038C 03B 4 000 1	AD SB MUL DIV ABT	EQU 03D7H EQU 03D4H EQU 038CH EQU 03B4H EQU 01H	;TO BCD DATA STRING ;F.P. ADDITION ;F.P. SUBTRACTION ;F.P. MULTIPLY ;F.P. DIVIDE ;COMMAND EXIT
000D 001A	SCHAR SUBT	EQU ODH EQU IAH	;(CONTROL A) ;STOP CHAR ;CHARACTER SUBSTITUTION ;(CONTROL Z)
10A0 10FD 02B7 02CA 1040 1058 1070 0F00	STACK JUMP TEST BLANK DOPND STORE RESLT TABLE	EQU 10A0H EQU 10FDH EQU 02B7H EQU 02CAH EQU 1040H EQU 1058H EQU 1070H EQU 0F00H	VARIABLE STORAGE VECTORED JUMP LIGHT TEST DISPL BLANKING DECIMAL OPERAND BUFFER TEMP BUFFER DECIMAL ANS BUFFER TABLE OF VALID COMMAND CODES AND VECTORS. OPERATIONS
1000	SCANB	EQU 10COH	BUFFER CONTAINING CHANNEL SCAN INFO.
1080 007F 0E8E	RBOUT MTEST	EQU 1080H EQU 7FH EQU OE8EH	CONVERSION FACTORS RUBOUT CHAR RAM MEMORY TEST
		GES USED BY THIS E END OF THE PROG	SECTION ARE ANNOTATED
2600 2603 2648		EQU 2600H EQU 2603H EQU 2648H	
0000 C0 0001 06FF 0003 51 0004 46B702	QUIET:	DB OCOH MVI A,OFFH OUT 8 CALL TEST	;FIRST INSTR = NOP ;RESET TTY BREAK ;CHECK DISPLAY
0007 464500 0007 468E0E	WAIT1:		;WAIT FOR OPERATOR ;CHECK RAM



	46CA02 462F03	BOOT:	CALL BLANK CALL INIT	;CHECK DISPLAY ;INITIALIZE MATH PAC ;AND DEFAULT VALUES.	
0017 0019 001D 001F 0023	2E 10367D 3E 0D 2E 1036C0 3E 2A 2E 1036B0 3E 2A 2E 103680		LXI H,RESLT+13 MVI M,SCHAR LXI H,SCANB MVI M, **' LXI H,STACK+16 MVI M, **' LXI H,CFBUF	;INVALID SCAN FLAG ;"WAIT" FLAG STORAGE ;15MS DEFAULT FLAG ;FILL CONVERSION FACTOR	
002B 002D 002E 002F 0032		L00P1:	MVI B,32 MVI M,O INR L DCR B JNZ LOOP1 LXI H,CFBUF MVI B,8	;BUFFER WITH 1.0	
0038 003A 003C 003D 003E 003F	3E81 0604 86 F0 09 483800	L00P2:		;1.0=8100000H	
0042 448D00 JMP MON ;START 'CI' CONSOLE INPUT ROUTINE INPUT: NO RESTRICTIONS REGISTERS: A,B,C,D OUTPUT: A,B ASCII C,D = 0					
0045	1A	ĊI:	IN 4 ;GET STA RAR JC CI	ART BIT	
0047 604500 004A 466A00 004D 1608 004F 466500 0052 49 0053 1A 0054 C1 0055 1A 0056 C8	466A00 1608 466500	START: RX:	CALL HALF MVI C,8 CALL DELAY IN 4	;1/2 DELAY ;BIT COUNT ;CENTER OF NEXT BIT	
	C1 1A		RAR MOV A,B RAR	;ROT INTO CARRY ;GET BUILD-UP WORD	
0057 0058 005B 005D	11 484F00 247F		MOV B,A DCR C JNZ RX ANI 7FH MOV B,A CALL DELAY	;STORE ;C=C-1 ;CHECK FOR LAST BIT ;MASK OFF PARITY	
	466500	;	CALL DELAY RET		
	1EC5 446C00	; DELAY	Y' TTY DELAY LOOP PARAMETER IN 'D' DES 9MS DELAY. MVI D,0C5H JMP TIME		



006A 1E62 HALF: MVI D,062H ;1/2 BIT TIME TIME: DCR D 006C 19 006D 486C00 JNZ TIME 0070 07 RET 'CRLF' OUTPUTS A CARRIAGE RET AND ; : LINE FEED. : INPUT: NO RESTRICTIONS ; REGISTERS: A,B,C,D ; OUTPUT: A=FFH.B=OAH:C.D=O CRLF: MVI B,ODH ;CR 0071 OEOD 0073 467000 CALL ĆO MVI B.OAH 0076 OEOA ;LF 0078 467000 CALL CO 007B 07 RET ; ; 'CO' CONSOLE OUTPUT ROUTINE : INPUT: WORD IS 7 BIT ASCII STORED IN B : REGISTERS: A.B.C.D ; OUTPUT: A=FFH, B IS SAVED, C,D = O CO: MOV A,B ORA A MVI C,11 SEND: OUT 8 CALL DELAY RAR COSITION NEXT BIT CPI OFFH DCR C SEND : SEND SEND IF NOT DONE 007C C1 ćo: 007D BO 007E 160B 0080 12 0081 51 0082 466500 0085 1A 0086 3CFF 0088 11 0089 488100 008C 07 RET : MONITOR ENTRY POINT AFTER POWER ON OR RESET. CALL CRLF ;RESET CARRIAGE LXI H,LBOOT ;START INFORMATION MON: 008D 467100 0090 2E263648 0094 46E800 0097 467100 CNTRL CALL LIST CNTRL: CALL CRLF 009A 2E263600 LXI H, READY ; ACK 009E 46E800 CALL LIST : GET COMMAND WORD AND FORM JUMP VECTOR CALL GET MVI B,O LXI H,DOPND MOV A,M JZ SRCH ADD B MOV B,A INT CHECK SUM ; INIT CHECK SUM ; POINT TO INPUT BUFF ; FETCH CHARACTER ; LOOK-UP TABLE ; ELSE BUILD CHECK SUM ; STORE CK SUM . RECOG: 00A1 46F800 00A6 2E103640 00A4 0E00 00AA C7 RLOOP: 00AB 3COD 00AD 68B600 00BO 81 00B1 C8 00B2 30 INR L



					;POINT TO LOOK-UP
	OOBB	3000	Siterie.	CPI O JZ ERR	;VALIDITY CHECK
	0000			CMP B	COMPARE CHECK SUM WITH
	00C4			INR L	; TABLE. IF TRUE, JMP. ;ELSE GET NEXT
	00C5 00C6			INR L INR L	
	00C7 00CA	44BAOO 30	VCTR:	JMP SRCHL INR L	;POINT TO LOW ADD
	00CB 00CC			MOV B,M INR L	SAVE POINT TO HI ADD
	OOCD			MOV C,M LXI H,JUMP	SAVE POINT TO JUMP LCN
	00D2	3E44		MVI M, 44H	
	00D4 00D5	F9		INR L MOV M,B	;LOAD VECTOR
	00D6 00D7	FA		INR L MOV M,C	
	00D8 00DB	467100 44FD10	EXEC:	CALL CRLF JMP JUMP	;EXECUTE
1	OODE	2E263603 46E800	ERR:	JMP JUMP LXI H,LERR CALL LIST	;ERROR MSG OUT
		449700	;		;TRY AGAIN
			; THIS F	ROUTINE IS USED T GS OF ALFA-NUM CH	
	00E8 00E9		LIST:	MOV B,M MOV A,M	;OUT REGISTER
(	OOEA	3COD		CPI SCHAR RZ	;IF DONE RET
(		467000		CALL CO	PRINT CHAR
(		48E800		INR L JNZ LIST	;POINT TO NEXT ;CHECK FOR PAGE WRAP
	00F4 00F5	28 44E800		INR H JMP LIST	;GET NEXT
				IS USED TO LOAD	
				OOPND OR LABELS I ISIRED BUFFER. IN	
				AS THE STOP CHAR. CANNOT START AT	
			; DOES N	OT ECHO A CARRIA IT' ERASES PREVIO	GE RET.
			; CHARAC	TERS IN SUCCESSI	ON.
			; CONTEN	ITS OF THE NEXT M	EMORY LOCATION.
,	1050	05107640	;		
	DOF8 DOFC		GETD:		;DEFAULT BUFFER ;DESIRED BUFFER ENTRY



00FD 464500 0100 3C7F 0102 682C01 0105 3C01 0107 689700 010A 3C1A	CNTU:	CALL CI CPI RBOUT JZ ERASE CPI ABT JZ CNTRL CPI SUBT	;SAVE LO POINTER ;TTY INPUT ;IF RUBOUT ;THEN ERASE ;COMMAND EXIT ;DISPLAY NEXT CELL
010C 681E01 010F 3C0D 0111 481601 0114 F8 0115 07 0116 467C00 0119 F9 011A 30		JZ CNTU4 CPI SCHAR JNZ CNTU1 MOV M.A	;SUBSTITUTION. ;IF DONE RETURN ;STORE STOP CHAR ;ECHO OUT :LOAD INPUT
OUF C7	CNTU4:	MOV A,M CPI SCHAR CZ CRLF+5 MOV B,M CALL CO	<b>y</b>
0129 44FD00 012C 31 012D C6 012E BC 012F 403301 0132 30 0133 CF 0134 467C00	; 'ERAS ; INCOR ERASE:	JMP CNTU E' IS USED TO RU RECTLY ENTERED C. DCR L MOV A,L CMP E JNC ECHO INR L MOV B,M CALL CO	;GET NEXT B OUT AN
0137 44FD00	; SPECI ; FLOAT	JMP CNTU P' CHANGES ASCII AL BDC USED BY TH ING POINT ROUTINN UST POINT TO BUFH	HE E
013A C7 013B 3C0D 013D 2B 013E 1430 0140 F8 0141 30 0142 443A01	; 'DISP'	CPI SCHAR RZ SUI 30H MOV M,A INR L JMP STRIP Y' CONVERTS SPECT THE FLOATING POIN	;POINT ;GET NEXT IAL BCD



0149 014A 014C 014D 014F	C7 3COD 2B 0430 F8	DISPL:	MOV A.M	;POINT TO BUFF ;FETCH ;IF DONE RET ;BCD TO ASCII ;STORE
	30 444901 C7 C8	; ; 'BINFI ; TO FL	JMP DISPL P CHANGES RA DATING POINT	AW BINARY DATA ;FETCH HI BYTE /
0156 0157 0158 0159 0158	30 D7 A0 2611 706D01	SHIFT:	INR L MOV C,M ANA A MVI E,17 JM EXIT	;FETCH LO BYTE ;CLEAR CARRY ;RESET COUNT ;IF NEGATIVE ;DATA IS NORM
015E 015F 0160 0163 0164 0165 0166 0167	C8 21 688201 C2 A0 12 D0 C1		MOV B,A DCR E JZ DZER MOV A,C ANA A RAL MOV C,A MOV A,B RAL	;FETCH HI BYTE ;FETCH LO BYTE ;CLEAR CARRY ;RESET COUNT ;IF NEGATIVE ;DATA IS NORM ;SAVE HI ;E=E-1 ;IS O ;LOAD LO BYTE ;CLEAR CARRY ;TWO BYTE SHIFT ;TO THE LEFT ;SET CNTRL BITS ;NEXT SHIFT ;POS NUM MASK
0173 0174 0175	F8 30 FA	EXIT:	MOV M,A INR L MOV M,C	SET CNTRL BITS NEXT SHIFT POS NUM MASK MS FP BYTE NEXT FP BYTE
0176 0177 0179 0178 017C 0180 0181	30 3E00 067F 84 2E103658 F8 07		INR L MVI M,O MVI A,7FH ADD E LXI H,STORE MOV M,A RET	;LS FP BYTE ;EXP ADJUST ;BIAS-#SHIFTS ;STORE EXP ;NORMAL EXIT
0182 0186 0188	3E00	DZER:	LXI H,STORE MVI M,O RET	;DATA=O ;O EXIT
0189 018A	1480	; TO BIN ; OF FP	ARY. HL MUSI DATA UPON EN SULT IS IN E MOV A,M SUI 80H	STRIP EXCESS 80H
018C 018D			MOV B,A MVI A,16	;SAVE ;2 byte bias



018F	91		SUB	В
0190	C8		MOV	B,A
0191	30		INR	L
0192	C7		MOV	Α,Μ
0193	3480		ORI	80H
0195	D8		MOV	D,A
0196	30		INR	L
0197	E7		MOV	E,M
0198	C3	CNTU3:	MOV	A,D
0199	BO		ORA	A
019A	1A		RAR	
019B	D8		MOV	D,A
0190	C 4		MOV	A,E
019D	1A		RAR	
019E	EO		MOV	E,A
019F	09		DCR	B
01A0	489801		JNZ	CNTU3
01A3	07		RET	
0000		END		

;COMPUTE # SHIFTS ;SAVE

;FORM MSBYTE ;SAVE IT

;GET LSBYTE ;RESTORE ;SHIFT 2 BYTES

;CHECK COUNTER



	USED DISPL 4 SIG ALSO BLANK	AY LIGHT ROUTINE TO OUTPUT VOLTAG AY LITES. DATA I SNIFICANT FIGURES CONTAINS THE LIT FUNCTIONS WHICH OF SYSTEM BOOT.	E DATA TO THE S OUTPUT IN S. THIS SECTION TE TEST AND LITE
0000	•	ORG O1COH	
	; EQUAT ; CAN B	ES NOT ANNOTATED DE FOUND IN PREVI	IN THIS SECTION OUS SECTIONS
1070 00FD 00FE 00F0 106C 006C 0030 000A 00F0 00E0 00D0 00B0 00B0	MINUS DECPT SPACE SSTAT STAT PLO	EQU O6CH EQU O3OH	DECIMAL RESULT 
		T SIGN AND STORE	AS A STATUS WORD
01C0 2E103670 01C4 C7 01C5 366C 01C7 3CFD 01C9 68D201	DISPL:	MOV A,M MVI L,STAT	;POINT TO RESULT ;FETCH SIGN ;IF - JUMP
01CC 0610 01CE 55	SETP:	MVI A,10H OUT 2+8	;+ AND DISABLE
01CF 44D501 01D2 0630 01D4 55	SETM:	JMP CNTU MVI A,30H OUT 2+8	;- AND DISABLE
01D5 F8	CNTU: ;	MOV M,A	;STORE STATUS
	; FETCH	DIGITS FROM RES UTPUT TO DISPLAY	ULT BUFFER, LIGHTS
01D6 3671 01D8 C7 01D9 3CFE 01DB 685802 01DE DF	,	MOV A,M CPI DÉCPT	;GET FIRST DIGIT ;IF DP, ADD LEADING O'S ;SAVE FIRST DIGIT
01DF 30 01E0 C7		INR L MOV A,M	;FETCH NEXT
01E1 3CFE 01E3 682602		CPI DÉCPT JZ LZERO	;IF DP, JUMP

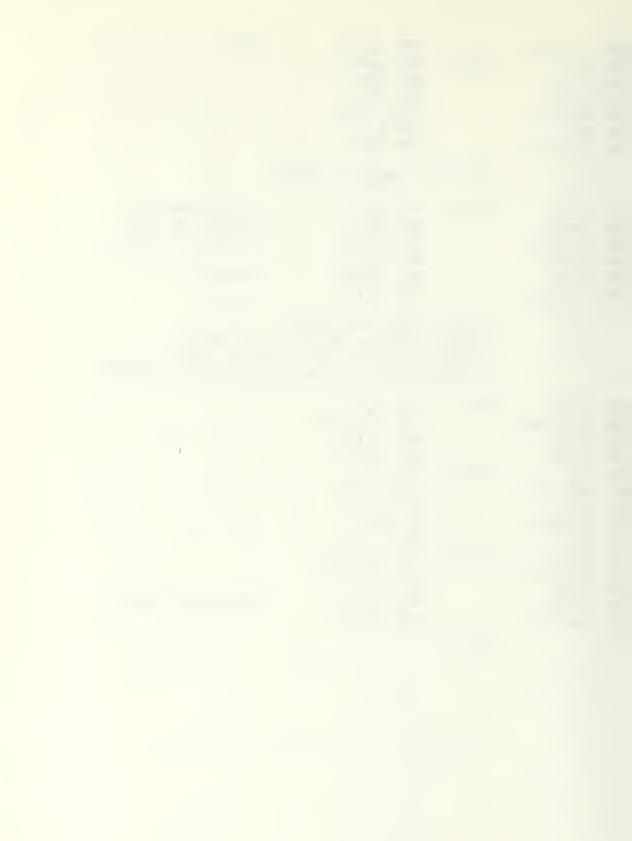


01E8 01ED 01F0 01F2 01F5 01F5 01F7 01FA 01FC 01FF 0201	0E00 460902 3CFE 687F02 0E01 460902 3CFE 688A02 0E02 460902 3CFE 689502 0E03		MVI B,0 CALL NODP CPI DECPT JZ DP1 MVI B,1 CALL NODP CPI DECPT JZ DP2 MVI B,2 CALL NODP CPI DECPT JZ DP3 MVI B,3	;ELSE OUTPUT FIRST DIGIT ;CHECK 2ND DIGIT
0206	440902	;	JMP NODP	;EXIT
020B	461302 DF 30 C7	; OUIPU. ; NODP:	T A DIGIT WITH N MVI A,DOM ORA D CALL LITE MOV D,M INR L MOV A,M RET	;ATTACH DATA ;OUT ;SAVE DIGIT ;FETCH NEXT
		; ; OUTPUI ; REGISI	I AND LATCH IER B_CONTAINS M	UX ADD OF LITE
0213 0214 0216 0217 021B 021C 021C 021D 021E 0220 0221 0223 0224 0225	2CFF 57 2E10366C C7 B1 55 2C10 55 2C10 55 F4		MOV E,L XRI OFFH OUT 8+3 LXI H,SSTAT MOV A,M ORA B OUT 8+2 XRI 10H OUT 8+2 XRI 10H OUT 8+2 XRI 10H OUT 8+2 XRI 10H OUT 8+2 RET	;SAVE POINT ;DATA OUT ;GET STATUS ;ATTACH MUX INFO ;MUX AND SIGN OUT ;SET LATCH ;LATCH OFF ;RESTORE
		; TO CHA	LEADING ZEROS NGE FROM SCIENT ED POINT.	IF NEEDED IFIC NOTATION
022E	C7 3CF0 483302 3672 445C02 DF	LZER 1:	MVI L,07CH MOV A,M CPI SPACE JNZ LZER1 MVI L,072H JMP DPO MOV D,M MVI L,71H	;GET EXP ;CONTINUE IF NOT ' ' ;RESTORE



0238 023A 023D 023E 0240 0243 0244 0245 0248 0249 0248 0249 0248 0245 0252 0253 0255 0258	06F0 461302 19 19 689D02 08 06F0 461302 19 687702	ZERO:	MVI B,0 MVI A,DPOL CALL LITE INR B MVI A,DOM CALL LITE DCR D JZ OUTZ2 INR B MVI A,DOM CALL LITE DCR D JZ OUTZ1 INR B MVI A,DOM JMP LITE MVI L,71H MVI D,0	;0 + 1 DP ;0 + NO DP ;EXIT ;DATA
		; INDIV	IDUAL DIGIT OUTF	015
025E 0260 0261 0264	461302 30	DPO: OUT3:	MVI B,0 MVI A,DPOL ORA D CALL LITE INR L	;ATTACH DATA
0268 026A 026D	34F0 0E01 461302 30	OUT2:	MOV A,M ORI DOM MVI B,I CALL LITE INR L	;ATTACH NO DP
0271 0273 0276 0277 0278	34F0 0E02 461302 30	OUTI: OUTZI:	MOV A,M ORI DOM MVI B,2 CALL LITE INR L MOV A,M ORI DOM MVI B,3	
027C 027F 0281	441302 0E01 06D0	DP1:	JMP LITE MVI B,1 MVI A,DPIL	;EXIT
	461302 446D02 0E02 06B0 B3	DP2:	ORA D CALL LITE JMP OUT2 MVI B,2 MVI A,DP2L ORA D CALL LITE	;ATTACH DATA
028F 0292 0295 0297 0299	0E03 0670	DP3:	CALL LITE JMP OUTI MVI B,3 MVI A,DP3L ORA D	

029A 441302 029D C7 029E 34F0 02A0 0E02 02A2 461302 02A5 30 02A6 447602	OUTZ2:	JMP LITE MOV A,M ORI DOM MVI B,2 CALL LITE INR L JMP OUTI	;EXIT
	, OUTPU	T CHANNEL NUMBER	S
02A9 0E07 02AB 34F0 02AD 461302	CLITE:	MVI B,7 ORI DOM CALL LITE	;LITE ADDRESS ;ATTACH NO D.P. ;OUTPUT CHANNEL #
02B0 0E06 02B2 06F0 02B4 441302		MVI B,6 MVI A,DOM JMP LITE	;OUTPUT 'O'
	; CALLE ; ACCUM	TEST AND BLANK D DURING POWER-U CONTAINS THE DI TER B CONTAINS T	SPLAY DATA.
02B7 0E07 02B9 2E10366C 02BD 3E10 02BF 1E0A	ŤEST:	MVI B,7 LXI H,SSTAT MVI M,10H MVI D,LT	
02C1 C3 02C2 461302 02C5 09 02C6 50C102 02C9 07	TESTL:		
02C9 07 02CA 0E07 02CC 2E10366C 02D0 3E30 02D2 1EFF	BLANK:	MVI B,7 LXI H,SSTAT M,PLO MVI D,OFFH	TURN OFF LITES
02D4 44C102 0000	END	JMP TÉSTL	



PROGRAM SPACE 0300H TO 07FFH IS USED FOR THE FLOATING POINT PACKAGE. APPENDIX C PRESENTS EXCERPTS FROM THE INTEL USERS LIBRARY.

END

;



	; THIS ; DRIVE ; ACQUI ; "READ	THE 'DATEL' (SEE SION MODULES. ALS " ROUTINE WHICH O ETER OUTPUT ON TH	SO INCLUDED IS THE
0000		ORG 0800H ES NOT ANNOTATED OUS SECTIONS.	CAN BE FOUND IN
0009 0007 0005 106D 106F 000D 00E8	MBYTE RAW MUXCH	EQU 9 EQU 7 EQU 5 EQU 106DH EQU 106FH EQU 0DH EQU 00E8H	;OUTPUT PORT 1 ;INPUT PORT 1 ;INPUT PORT 2 ;RAW DATA INPUT BUFFER ;DEFAUT MUX CHANNEL STORE ;STOP CHAR ;OUTPUT 1 LINE ;OF TEXT
0071	CRLF	EQU 0071H	;OUTPUT CARRAGE RET- :LINE FEED
00F8	GET	EQU OOF8H	;INPUT DATA FROM OPERATOR
013A 0154 01C0	STRIP BINFP DISPL	EQU 013AH EQU 0154H EQU 01COH	BCD←ASCII F.P.←BIN OUTPUT DATA TO LIGHT DISPLAY
0249	CLITE	EQU OZA9H	;OUTPUT CHAN # IN ; A' TO DISPAY LIGHTS
OFF 4 OFF 8 OFF C 1 0 40 1 058 1 0 70 0 70F 0 3 6 E 0 3 3 E 0 3 D 7 0 3 D 4 0 3 8 C 0 3 B 4	STORE	EQU OFF4H EQU OFF8H EQU OFFCH EQU 1040H EQU 1058H EQU 1070H EQU 070FH EQU 036EH EQU 033EH EQU 03D7H EQU 03D4H EQU 038CH EQU 03B4H	; A 10 DISFAT LIGHTS ; 10.0 ; 819.15 ; 8191.5
2672	MESSA LREAD	GES EQU 2672H	
'SHOLD' EXECUTES ONE SAMPLE AND HOLD CYCLE, AND INPUTS A DOUBLE BYTE OF RAW DATA. INPUT: ACUM CONTAINS MUX CHANNEL H,L POINT TO LSBYTE STORAGE			



			TERS: A,L T: 'A' DESTROYED	, L=L-Í
0800 0802	3410	SHOLD:	ORI 10H OUT CMD ORI 18H OUT CMD	;RESET A/D MODULE
	3418			;SAMPLE
0806	2010		XRI 10H OUT CMD	;HOLD
	4B 2440 480908	READ1:	IN MBYTE ANI 40H JNZ READI	;CHECK FOR END OF CONVERT
0812 0813 0814 0815 0815 0817 0819	2CFF F8 31 4B 243F 2C3F F8		IN LBYTE XRI OFFH MOV M,A DCR L IN MBYTE ANI 3FH XRI 3FH MOV M,A	;INPUT RAW DATA
081A	07	;	RET	
		; TO VO ; TO TH	CHANGES RAW FLOA LTAGE UNITS PROPO E RAW DATA FROM 1 RMS THE INVERSE O	DRTIONAL THE A/D. "VRI"
	2E103658 466E03	ŔVI:	LXI H,STORE Call Lod	;A←M
0822	2E0F36F8 46B403		LXI H,I3 Call DIV	;A+A/819.15
	2E0F36F4 46D403 07		LXI H,I2 CALL SB RET	;A+A-10.0
0831 0835	2E103658 466E03	VRI:	LXI H,STORE CALL LOD LXI H,I3	; A ← M
083C	2EOF36F8 468C03 2EOF36FC		CALL MUL LXI H,I4	;A←A*819.15
	46D703		CALL AD RET	;A+A+8191.5
		; ANY DE	' IS USED FOR CAL ESIRED CHANNEL. C AL DISPLAY IN VOL	DUTPUT IS ON THE
	2E263672 46E800	READ:	LXI H,LREAD Call list	;PROMPT
084E 0851	46F800 467100		CALL GET CALL CRLF	;INPUT CH. NO.
0858	2E 103640 463A01 2E 103640 C7		LXI H,DOPND CALL STRIP LXI H,DOPND MOV A,M	;BCD←ASCII



0860 2E10366F 0864 F8		LXI H,MUXCH Mov M,A	;STORE CH. NO.
		CALL CLITE	;DISPLAY CH. NO.
0868 2E10366F	NEXI:		;CONTINUE
086C C7		MOV A,M	;RESTORE CH. NO.
086D 2E10366E			;POINT TO LSBYTE STORE
		CALL SHOLD	;GET DATA
0874 465401		CALL BINFP	;CONVERT TO F.P.
		CALL RVI	;CHANGE TO VOLTS
		LXI H,RESLT	
087E 460F07		CALL OUU	;BCD←F.P.
0881 460001		CALL DISPL	DISPLAY
0884 49		IN 4	SCAN TTY FOR INT
			SET CARRY FLAG
0886 606808		JC NEXT	; IF NO INT, GET
			MORE DATA FROM
			SAME CHANNEL
0889 467100		CALL CRLF	ELSE PROMPT
088C 444708		JMP READ	,
0000	END		

	THESE ROU LABELS AN FOR ANNOT IN ADDITI PROVIDE F GROUPS OF	D TO PROVIDE ATING ANY GI ON, THE "WAI	ED TO UPDATE NUMERICAL EDITING CAPABILITY VEN RUN. T" ROUTINES TIME DELAYS BETWEEN
0000	; OR (	HOASO H	
	; FOUND IN	NOT ANNOTATED PREVIOUS SEC	
000A 000D 1F00 0097 0065 007C 00A1 00FC 0071 00E8 00F8 013A 1040 1070 10A0 00A5	SCHAR EQU BHEAD EQU CNTRL EQU DELAY EQU CO EQU RECOG EQU CRLF EQU LIST EQU STRIP EQU DOPND EQU RESLT EQU STACK EQU	0097H 0065H 007CH 00A1H 00FCH 0071H 00E8H 00F8H 00F8H 013AH 1040H	LINE FEED STOP CHAR BUFF FOR "FILE" SYSTEM MONITOR KILL TIME LOOP CONSOLE OUT COMMAND RECOGNITION INTO ANY BUFFER :LSB OF COORD NO.
JON J	; MESAGES	- ON M	,200 01 00010 100
2603 267E 2629 2793 279C	LWAIT EQU LERR2 EQU LRUN EQU LCOM EQU		
		PDATES THE C A GIVEN RUN	
08A0 2E1036A5	COORD: LXI	H,STACK+5	;LOAD COORDINATION ; NUMBER INTO
08A4 C7 08A5 31 08A6 D7 08A7 31 08A8 DF 08A9 0401	DCR MOV DCR MOV	С,М	; A,B,C,D ;INCREMENT AND
08A9 0401 08AB 3C3A		3 <b>A</b> H	; DECIMAL ADJUST ;>= ASCII 10 ?



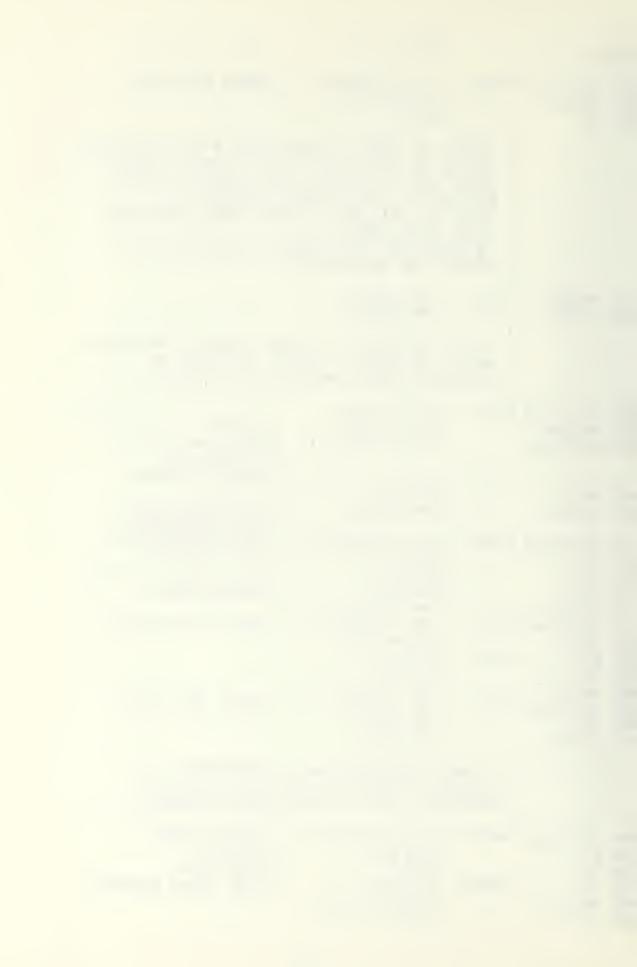
0880 0881 0884 0886 0887 0889 0888 0885 0862 0862 0862 0865 0865 0865 0865 0865 0865 0865 0865	44CD08 0E30 C2 0401 3C3A 40C208 D0 44CD08 1630 C3 0401 3C3A 40EB08 D8 36A5 F9 31 FA 31 FA 31 FA 31 FB CF 467C00 30 CF	ADJ1:	JNC ADJ1 MOV B,A JMP DONE1 MVI B,°O° MOV A,C ADI 1 CPI 3AH	;JUMP IF TRUE ;ELSE RESTORE ;EXIT ;RESET UNITS DIGIT ;CHECK 10'S DIGIT
		ADJ2:	JNC ADJ2 MOV C,A JMP DONE1 MVI C, °O° MOV A,D ADI 1 CPI 3AH	;RESET 10'S DIGIT ;CHECK 100'S DIGIT
		DONE1:	JNC ADJ3 MOV D,A MVI L,NEWNO MOV M,B	;STORE NEW NUMBER
			DCR L MOV M,C DCR L MOV M,D MOV B,M CALL CO INR L MOV B,M CALL CO INR L MOV B,M CALL CO MVI B,	;OUTPUT NEW NUMBER
08E7 08EA			CALL CO CALL CO RET	BACK TO CALLER
	1 E30 44CD08	ADJ3:	MVI D, 'O' JMP DONE1	;OVERFLOW
		; IT IS ; ENABLI	O" PRINTS OUT 'R ENTERED WITH 'C ES THE USER TO I UMBER INFO.	CONTROL R', AND
			LXI H,LRUN Call list	; 'RUN NO. '
08F7 08FB	46E800 2E113600 46FC00 449700		LXI H,1100H CALL GETD JMP CNTRL	;BLANK PAGE
		,	" IS USED TO FLA LINE COMMENT.	G AND ENTER
-	2E27369C 46E800		LXI H,LCOM Call list	;*****
0908	2E113600		LXI H,1100H	;BLANK PAGE



	46FC00 449700		CALL GETD JMP CNTRL	;INPUT COMMENT ;WHEN DONE, JUMP
	-	LATER ENTRY CONT AFTER IS US RECOR RUBO EXIST CHARA	PRINT OUT. USES TO TERMINATE TH ROL F" IS USED T EDITING AN EXIS ED TO STEP FOREW D IN ORDER TO SU UT" IS USED TO S ING RECORD IN OR	E RECORD. O EXIT THE ROUTINE ONLY TING FILE. "CONTROL Z" ARD THROUGH AN EXISTING BSTITUTE CHARACTERS. TEP BACKWARD THROUGH AN DER TO SUBSTITUTE ALWAYS PRECEEDS THE
0016	46FC00 467100 31 C7 3C0A 689700 30	ELOOP:	LXI H,BHEAD+1 CALL GETD CALL CRLF DCR L MOV A,M CPI LF JZ CNTRL INR L	
091C 091D				;FETCH LAST ENTRY
				; IF LF, THEN DONE
0923 0924			INR L INR L JMP ELOOP	;ELSE CONTINUE ENTRIES
		ENTER IT IS RECOR SEQUE TO PR IN THU WAS E	NCE. CONTAINS AN ENVENT AN INFINI E EVENT THAT THE	ROUTINE. TPUT MULTI-LINE H A SEPARATE LF CR OVERRUN PROTECTION TE OUTPUT LOOP FIRST CALL TO "EDIT" OL F" RATHER THEN LF.
092B 092E 0932 0935 0935 0938 093A 093A 093E 0941 0942 0943 0945 0948 0948 0948	3COD 684E09 3COA 685109 C8 467C00 30 C6	HDR: HLOOP: HL1:	CALL CRLF CALL CRLF LXI H,BHEAD+1 MOV A,M CPI SCHAR JZ NEXT CPI LF JZ DONE2 MOV B,A CALL CO INR L MOV A,L	
				;CHECK FOR EOL
				;CHECK FOR EOR
				;PRINT CHARACTER
				GET ANOTHER CHECK FOR OVERRUN
			CPI O JZ ERR2	,
		NEXT:	JMP HLOOP	;EOL
		DONE2:	JMP HL1 Call CRLF	



RET 0954 07 0955 2E263629 ERR2: LXI H, LERR2 ; ERROR MSG OUT 0959 46E800 CALL LIST 0950 07 RET "FILE" IS USED AS THE ENTRY POINT FOR THE ; OUTPUT OF THE MULTI-LINE RECORD ENTERED WITH "EDIT". "HLOOP" IS THE ENTRY POINT FOR MULTI-LINE RECORDS POINTED TO WITH HL. ALL SUCH RECORDS MUST END WITH A LF CR SEQUENCE. IN ADDITION, ALL SUCH RECORDS MUST NOT CROSS PAGE BOUNDARIES. FILE: CALL HDR 095D 462809 JMP CNTRL 0960 449700 "WAIT" IS USED TO STORE A DELAY PARAMETER ; WHICH IS USED BY "SCAN" IN ORDER TO ; PROVIDE A DELAY BETWEEN DATA POINTS. 0963 2E26367E WAIT: LXI H.LWAIT CALL HLOOP 0967 463209 :PROMPT RESET WAIT FLAG LXI H.STACK+16 096A 2E1036B0 : TO TURN OFF :DEFAULT OPTION 096E 3E00 MVI M.O JMP RECOG :GET WAIT FACTOR 0970 44A100 FROM OPERATOR LXI H,STACK+14 0973 2E1036AE MS25: STORE 25MS DELAY 0977 3E53 MVI M,83 FINE STORAGE 0979 31 DCR L 097A 3E02 MVI M.2 :COARSE DELAY 097C 449700 JMP CNTRL 097F 2E1036AE MS15: LXI H, STACK+14 :STORE 15MS DELAY 0983 3E62 0985 31 MVI M.98 EXIT: DCR L 0986 3E01 MVI M.I 0988 449700 JMP CNTRL LXI H, STACK+14 ;STORE 3MS DELAY 098B 2E1036AE MS3: MVI M,23 098F 3E17 JMP EXIT 0991 448509 "VWAIT" VARIABLE WAIT SUBROUTINE : CALLED BY THE SCAN ROUTINE TO PROVIDE PROVIDE A DELAY BETWEEN DATA POINTS. 0994 2E1036AD VWAIT: LXI H,STACK+13 :COARSE DELAY MOV E,M :COUNTER 0998 E7 INR L ;FINE DELAY 0999 30 MOV D,M 099A DF VLOOP: :FINE DELAY COUNTER 099B 466700 CALL DELAY+2 DCR E 099E 21



099F 09A2	489A09 07		JNZ Ret	VLOOP	
		; OUTPU ; RESUL ; "NOEX ; + OR ; FOR V ; OR "N	T REA T BUF 4" AS - 999 OLTAC OEX4"	ADABILITY BY FER TO 4 SIO SSUMES DATA 1 99, AND IS US E OUTPUT. EI MUST BE CAN	NO LARGER THAN SED MAINLY ITHER "YESEX" LLED AFTER "ROUND".
		; DIGIT	S FRC .H.L	DM THE DISPLA DESTROYED, I	IPUT 4 SIGNIFICANT AY REGISTER. NO INPUT RESTRICTIONS.
09A9	3E00		MVI	E,2 H,STACK+15 M,0 H,RESLT+9	;ENTRY COUNTER ;RESET OVERFLOW BIT
09AF 09B0	2E103679 31 C7 3C2E	CONT1:	DCR MOV CPI	L A,M	;POINT TO LSD ;GET IT ;IF DP, JUMP
09B6 09B8			JZ C MVI DCR	ONT2	;ELSE INSERT CR ;NEXT
09BC 09BF	48AF09 44C609 31 3E30	CONT2:	JMP	SIG	;COMPUTÉ DIGITS
09C2 09C3	21 48BF09 31	SIG:	JNZ	CONT2	; IF E=O, DONE ; POINT AT DIGIT
	3C2E 48D909		CPI JNZ DCR		; TO BE OPERATED ON ;GET TRIAL DIGIT ;IF DP GET NEXT ;ELSE CONTINUE
09D1 09D3	3E30 3C35 600A0A		CPI JC E	M,30H	;INSERT O
09D9	44E009 3C35	CONT3:	CPI	35H	;IF < 5 DO NOT ; ROUND
09DE	2603	CONT5: CONT4:	MVI MVI	XITI M,SCHAR E,3 L	ELSE INSERT CR DIGIT COUNTER POINT NEXT
09E3 09E4	C7		MOV CPI	A,M	GET NEXT ;IF DP GET NEXT



09EB 09ED 09F0 09F2	0401 3C3A 60090A 3E30 21 48E209		ADI 1 CPI 3AH JC EXIT2 MVI M,3OH DCR E JNZ CONT4	;ELSE INCR DIGIT ;IF < 10 DONE ;ELSE RIPPLE CARRY ;NEXT DIGIT
09FA 09FD 09FF 0A01		LAST:	MOV A,M CPI ••• JZ LAST ADI 1 CPI 3AH JZ EXIT3	;GET LAST DIGIT ;IF DP GET NEXT ;INCR DIGIT ;IF OVERFLOW JUMP ;ELSE PRINT
0A07 0A09 0A0A 0A0E 0A0F 0A12 0A13 0A15 0A15 0A19 0A1A 0A1D 0A1F 0A22	060D F8 2E103670 CF 467C00 07 3E30 2E103670 CF 467C00 0E31 467C00 2E1036AF	EXIT2: EXIT5:	LXI H,RESLT MOV B,M CALL CO	;SIGN OUT
			MVI M,30H LXI H,RESLT MOV B,M CALL CO MVI B,'1' CALL CO LXI H,STACK+15	;INSERT O ;DISPLAY BUFFER ;SIGN OUT ;OVERFLOW DIGIT ;SET OVERFLOW FLAG
0A28	3E01 07	FORMAT < 1 >+ OR SMALL USED T VOLTAG	T TO DECIMAL FOR MUST NOT BE US	ED FOR RESULTS ROUTINE ASSUMES ONLY "E" FORMAT. EADABILITY OF
0A2D 0A2E 0A30 0A33 0A37 0A38 0A3A 0A3D 0A41	3C01 6A7E0A 2E103679 C7 3C45 48850A 2E10367C C7 2E1036AF 1431		MOV A,M CPI 1 CZ EXIT6 LXI H,RESLT+9 MOV A,M CPI 'E' JNZ EXIT7 LXI H,RESLT+12 MOV A,M	;CHECK FOR OVERFLOW ;OUTPUT CARRY ;CHECK FOR E FORMAT ;IF NO, NORMAL EXIT ;GET # DECIMAL PLACES ;SCRATCH ;# LEADING O'S ;SAVE



OA 4A         3CO 4         CPI 4           OA 4C         406FOA         JNC ZERO         ;IF >3 0'S, NUME	250-0
OA4C 406F0AJNC ZERO;IF >3 0'S, NUMEOA4F 466F0ACALL ZERO;OUTPUT LEADINGOA52 2E1036AFLXI H,STACK+15OA56 CFMOV B,M;RESTOREOA57 0604MVI A,4;# SIG DIGITS	0°S
OA56 CF MOV B,M ;RESTORE	
OA56 CFMOV B,M;RESTOREOA57 0604MVI A,4;# SIG DIGITSOA59 91SUB B;DIGITS REMAINING	NG
OA5A EO MOV E.A ;COUNTER	
OA5B 2E103671 DGOUT: LXI H,RESLT+1 ;SKIP SIGN OA5F CF DLOOP: MOV B,M	
OA60 467COO CALL CO :DIGIT OUT	
OA63 30 SKIPD: INR L OA64 C7 MOV A,M	
OA65 3C2E CPI '.' ;SKIP DP	
OA67 68630AJZ SKIPDOA6A 21DCR E	
OAGB 485FOA JNZ DLOOP	
DAGE 07 RET	r
OAGF OE2E ZERO: MVI B, '.' ;LEADING O'S OUT OA71 467COO CALL CO	
OA74 OE30 MVI B, 'O' OA76 467COO ZLOOP: CALL CO	
OA7A 48760A JNZ ZLOOP	
OA7D 07 RET OA7E 2E103675 EXIT6: LXI H,RESLT+5 ;TRUNCATE LS O	
OA82 3EOD MVI M,SCHAR	
OA84 07 RET OA85 2E103671 EXIT7: LXI H,RESLT+1 ;NORMAL EXIT	
0A89 44E800 JMP LIST ;RET THROUGH "LI	IST"
, "YESEX" IS USED TO RETAIN "E" FORMAT I	
; ORDER TO DISPLAY VERY LARGE OR VERY SM ; RESULTS. SIGN ASSUMED OUT.	IALL
;	
OA8C 2E1036AF YESEX: LXI H,STACK+15 ;CHECK FOR CARRY OA90 C7 MOV A,M	{
OA91 3CO1 CPI 1	
OA93 6A7EOA CZ EXIT6 OA96 2E103671 LXI H,RESLT+1 ;OUTPUT MANTISSA	
OA9A 46E800 CALL LIST	1
OA9D 2E103679 LXI H,RESLT+9 ;OUTPUT EXP OAA1 44E800 JMP LIST	
OAA1 44E800 JMP LIST 0000 END	



	128 SETS OF DATA CHANNELS OF DATA DEFINED TIME DEL TO UNITS DEFINED	IC NESSARY TO TAKE POINTS FOR UP EIGHT (IN ANY ORDER) WITH USER AY. THE RESULT IS CONVERTED BY THE USER.
	EQUATES NOT ANNO IN PREVIOUS SECT	TATED CAN BE FOUND Lons.
1050	FOPND EQU 1050H	;FLOATING POINT ;OPERAND BUFFER
1040	PAGE EQU IOAOH	HIGH ADD FOR RAW DATA STORAGE
10C0 00C0 10A0	SCANB EQU 10COH SCB EQU 0COH STACK EQU PAGE	CHANNEL SEQUENCE BUFF START OF SCAN BUFF START OF VARIABLE SCRATCH PAD
10A1	LINE EQU PAGE+1	;LOW ADD FOR RAW ;DATA STORAGE
10A2	CHNPT EQU LINE+1	POINTS TO A LCN IN THE SCAN STORAGE BUFF
0800 0800	CFB EQU 080H Shold Equ 0800h	START OF CF BUFFER SAMPLE/HOLD/CONVERT COMMANDS FOR A/D
0994 08A0 09A3	VWAIT EQU 0994H COORD EQU 08A0H ROUND EQU 09A3H	VARIABLE TIME DELAY UPDATE COORDINATION # ROUND OUTPUT BUFFER TO 4 SIG DIGITS
0A29	NOEX4 EQU OA29H	;CONVERT SMALL NOS. ;FROM "E" FORMAT
0480	YESEX EQU OA8CH	TO "F" FORMAT RETAIN "E" FORMAT WITH 4 SIG DIGITS FOR NUMBERS LT .1
0145 0045 081B	DISPY EQU 0145H CI EQU 0045H RVI EQU 081BH	;OR GT 9999999. ;ASCII←BCD ;CONSOLE INPUT ;RAW DATA TO VOLTS ;INTERPOLATION
OFE 4 00E8 0071 00F8 00FC 1040 1058 007C 0097 0154 036E 033E 03D7	I5       EQU OFE 4H         LIST       EQU 00E8H         CRLF       EQU 0071H         GET       EQU 00F8H         GETD       EQU 00FCH         DOPND       EQU 1040H         STORE       EQU 007CH         CO       EQU 007CH         CNTRL       EQU 0097H         BINFP       EQU 0154H         LOD       EQU 036EH         STR       EQU 03D7H	;.0078125

038C 1070 070F 013A 000D	000	EQU 038CH EQU 1070H EQU 070FH EQU 013AH EQU 0DH ORG 0ABOH	
	, MESSA	AGES	
26A7 26C7 26I0 26F3 26F7 26FF 2704 2730	C1 C2 C3 DWAIT EXP		
		3' TAKES 128 SE S AT VARIABLE I	
	•	DEFINITIONS	
	; I NR M	MACRO POINT,N LXI H,POINT MOV A,M ADI N MOV M,A ENDM	;INCREMENT A MEMORY ;LOCATION N TIMES
	; ; commo	N SUBROUTINES	
OABO 2E1036A2	2 INDF:	LXI H,CHNPT	;INDIRECT FETCH AND STORE ;IN 'A'. CHNPT CONTAINS ;LOW ADD. DATA ASSUMED ;ON SAME PAGE
0AB4 F7 0AB5 C7		MOV L,M MOV A,M	; ON SAME FAGE
0AB6 07 0AB7 2E1036A0	) INDP:	RET LXI H,PAGE	;INDIRECT POINTER ;STORED IN FIRST 2 ;STACK POSITS.
OABB DF OABC 30		MOV D,M INR L	;SAVE
OABD F7 OABE EB		MOV H,D	;LOW POINT ;HIGH POINT
UAC4 3E12	SCAN2:		;INITIALIZE ;DATA INPUT BUFFER
0AC6 30 0AC7 3E00 0AC9 30		INR L MVI M,O INR L	;FIRST STORAGE LCN



MVI M,SCB ;START OF INFO BUFFER OACA JECO RET OACC 07 SKIP: INRM CHNPT,1 ;IGNORE DELIMITER LXI H,CHNPT 0ACD 2E1036A2 MOV A,M OAD1 C7 ADI OOOOIH 0AD2 0401 MOV M.A OAD4 F8 CALL INDF ;TEST NEXT CHAR 0AD5 46B00A CPI 8 0AD8 3C08 OADA 40CDOA JNC SKIP OADD 07 RET OADE 2E1036AO SCAN4: LXI H,PAGE :RE-INIT BETWEEN SCANS 0AE2 3E12 MVI M, 12H **OAE4 30** INR L 0AE5 30 INR L OAE6 3ECO MVI M,SCB RET 0AE8 07 OAE9 46B70A DATA: CALL INDP ;POINT TO RAW STORAGE OAEC 465401 CALL BINFP ;CONVERT TO F.P. LXI H,STORE 0AEF 2E103658 :LOAD AND POINT : TO OPERAND OAF3 466E03 CALL LOD LXI H, FOPND OAF6 2E103650 OAFA 07 RET ; ; SCAN3: CALL INDF :GET DESIRED CHANNEL OAFB 46BOOA CPI SCHAR OAFE 3COD : IF ALL CHANNELS SCANNED 0B00 68220B JZ COUNT : SET UP NEXT STORAGE CPI 8 ;A>=8 ? OB03 3C08 CNC SKIP 0B05 42CD0A 0B08 46B70A :IF TRUE, JUMP CALL INDP INR L OBOB 30 CALL SHOLD ; INPUT RAW DATA OBOC 460008 INRM PAGE,1 LXI H,PAGE MOV A,M :NEXT CHANNEL OBOF 2E1036A0 MOV A.M OB13 C7 OB14 0401 ADI 00001H OB16 F8 MOV M.A INRM CHNPT.I :NEXT VECTOR POINTER OB17 2E1036A2 LXI H, CHNPT OBIB C7 MOV A.M ADI OOOO1H OB1C 0401 MOV M.A OBIE F8 ;GET NEXT DATA OBIF 44FBOA JMP SCAN3 COUNT: INRM LINE,2 ;NEXT STORAGE OB22 2E1036A1 LXI H,LINE MOV A.M 0B26 C7 0B27 0402 ADI 00002H MOV M.A 0B29 F8



OB2E	469409	RESET:	RZ CALL VWAIT CALL SCAN4 JMP SCAN3	;CHECK FOR END ;KILL TIME ;GET ANOTHER SET
		; CHANN	5' TAKES SETS O EL ASSINGMENT D SCAN' ROUTINE.	F DATA FROM THE EFINED BY THE
0B37 0B3A 0B3B			CALL COLMN CALL CRLF RET CALL CRLF CALL COORD	;PRINT COLUMN HEADINGS ;BACK TO CONTROLLER ;UPDATE COORDINATION
	46C00A	CNTU7:	CALL SCAN2	NUMBER INIT FOR NEXT SCAN ALSO ENTRY POINT FOR SCANNING WITHOUT COL HEADINGS
0B44 0B47 0B4A	46B00A	AVE:	CALL SCAN3 CALL SCAN2 CALL INDF	;TAKE SET OF DATA
0B4F 0B50 0B52	2B		CPI SCHAR RZ CPI 8 CNC SKIP LXI H,STACK+6	
	BO		MOV C,A ORA A RAL RAL MVI B,CFB	;FACTOR VECTOR. ;SAVE 'A' ;CLEAR CARRY ;MPY BY 4 ;START OF CONVERSION
	F8		MOV M,A MOV A,C CALL DATA	FACTOR BUFFER COMPUTE VECTOR STORE VECTOR RESTORE 'A' CONVERT AND STORE NEXT RAW DATA
OB6C	0402		INRM LINE,2 LXI H,LINE MOV A,M ADI 00002H MOV M,A	;NEXT RAW DATA
0B 70	68830B		JZ NEXTP	;IF DONE, OUT RESULTS ;SETUP FOR NEXT SCAN
	46E90A		CALL DATA	CONVERT AND STORE RAW DATA POINT
0B76	46D703		CALL AD	;ADD TO PREVIOUS



0B7D 0B80	463E03 44680B		LXI H,FOPND Call STR JMP AVEL LXI H,I5	;COMPUTE AVERAGE ; AND CONVERT TO
088A 088E 0891	466E03 2E103650 468C03 462208 2E1036A6		CALL LOD LXI H,FOPND CALL MUL CALL RVI+7 LXI H,STACK+6	:A←A*SUM
0B98 0B99	F7 468C03		MOV L,M CALL MUL	;CHANGE FROM VOLTS ;TO USER DEFINED UNITS
0BA0 0BA3 0BA6 0BA9 0BAD 0BAE 0BB0 0BB3 0BB6 0BB8	460F07 464501 46A309 2E1036B1		LXI H,RESLT CALL OUU CALL DISPY CALL ROUND LXI H,STACK+17 MOV A,M CPI 'Y' JZ EXP1 CALL NOEX4 MVI B, ' CALL CO CALL CO	CONVERT TO DECIMAL CONVERT TO ASCII 4 SIG DIGITS
	0401		INRM PAGE,1 LXI H,PAGE MOV A,M ADI 00001H MOV M,A	;P=P+1
OBC6 OBCA OBCB OBCD	0401		INRM CHNPT,1 LXI H,CHNPT MOV A,M ADI 00001H MOV M,A	;C=C+1
OBCE	444A0B	• •	JMP AVE	
OBD1	2E1036A2	COLMN:	LXI H,CHNPT	;LOAD POINTER WITH ;START OF SCAN BUFF
0B D 5 0B D 7 0B DB	2E1036B1		MVI M,SCB LXI H,STACK+17 MOV A,M	;CHECK FOR ;"E" FORMAT
OBE1 OBE5 OBE8	48F00B 2E2636F3 46E800		CPI 'Y' JNZ CNTUD LXI H,CI CALL LIST MVI B,SCHAR CALL CO	YES ? THEN CONTINUE ELSE JUMP CARRIAGE RETURN



OBED 44F70B JMP CLOOP OBFO 2E2636F3 CNTUD: LXI H,CI ; # \* OBFO2E2636F3CNTUD:LXI H,C1;" # "OBF446E800CALL LIST;" # "OBF746B00ACLOOP:CALL INDF;GET CHANNELOBFA3CODCPI SCHAR;RET WHEN DONEOBFC2BRZ;RET WHEN DONEOBFD3CO8CPI 8;IGNORE DELIMITER:OBFF42CD0ACNC SKIP;CONVERT TO ASCIIOC020430ADI 30H;CONVERT TO ASCIIOC04E0MOV E,A;SAVE CH. NO.OC052E1036B1LXI H,STACK+17;CHECK FORMATOC09C7MOV A,M;CHECK FORMATOC043C59CPI 'Y';IF TRUE, "E" FORMOC04S2B0CJZ EXP2;IF TRUE, "E" FORM ;RET WHEN DONE ;IGNORE DELIMITERS 0C05 2E1036B1 0C09 C7 0C0A 3C59 ; IF TRUE, "E" FORMAT ADJUST HEADINGS 

 OCOF 2E2636F7 CNTU9:
 LXI H,C2
 ; CH.

 OC13 46E800
 CALL LIST
 ; CH.

 OC16 CC
 MOV B,E
 ; RECALL CH. NO.

 OC17 467CO0
 CALL CO
 ; OUTPUT

 INRM CHNPT,1
 ; C=C+1

 OC18 C7
 MOV A,M

 OC1F 0401
 ADI 00001H

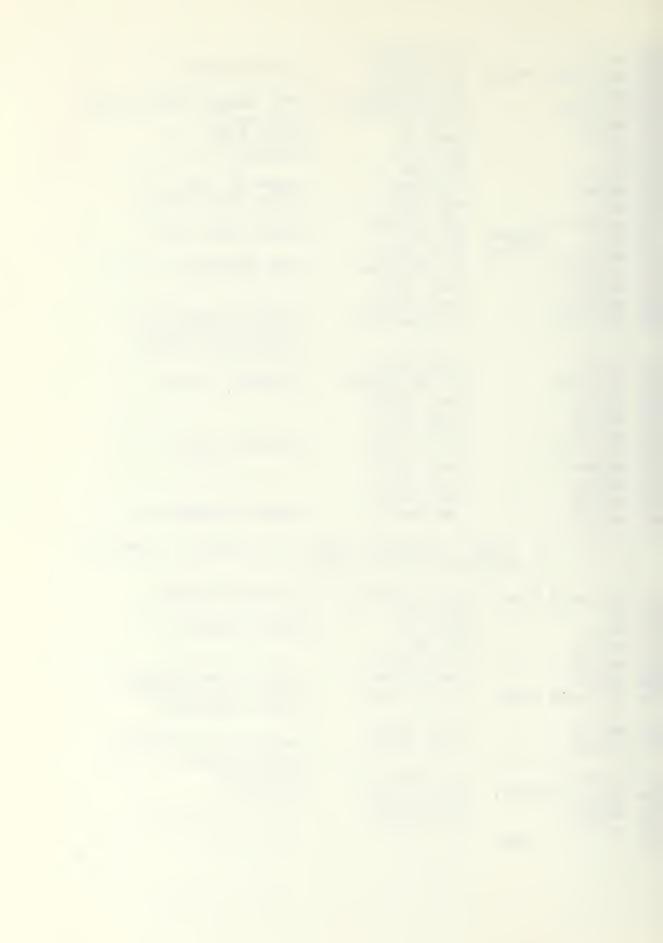
 OC21 F8
 MOV M,A

 OCOF 2E2636F7 CNTU9: LXI H,C2 ; CH. \* OC22 44F70BJMP CLOOPOC25 468COAEXP1:CALL YESEXOC28 44B60BJMP CNTU8 OC2B 2E2636FF EXP2: LXI H,C3 ;"E" FORMAT COL ADJ 0C2F 46E800 0C32 440F0C CALL LIST JMP CNTU9 SSCAN' SET SCAN ENTRY POINT. PRINTS ; INSTRUCTIONS AND STORES THE NUMBER ; AND SEQUENCE OF CHANNELS TO BE SCANNED. ; IT ALSO SETS THE COORDINATION NUMBER TO ZERO. OC35 2E1036A5 SSCAN: LXI H,STACK+5 ;RESET COORD # OC352E1036A5SSCAN:LAT H,STACKTSHEEL CONTROLOC393E30MVI M,'O'OC3B31DCR LOC3C3E30MVI M,'O'OC3E31DCR LOC3F3E30MVI M,'O'OC412E1036B0PRM1:LXI H,STACK+16OC412E1036B0PRM1:LXI H,STACK+16OC45C7MOV A,MOC463C2ACPI '\*'OC48485E0CJNZ CNTU5OC4B2E1036ADLXI H,STACK+13CH2CKFOR WAITFLSELOADDEFAULTSTACK+13CF15MS :OF 15MS MVI M,1 INR L MVI M,98 LXI H,DWAIT ;INFORM OPERATOR CALL LIST 0C4F 3E01 0C51 30 0C52 3E62 0C54 2E273604 0C58 46E800



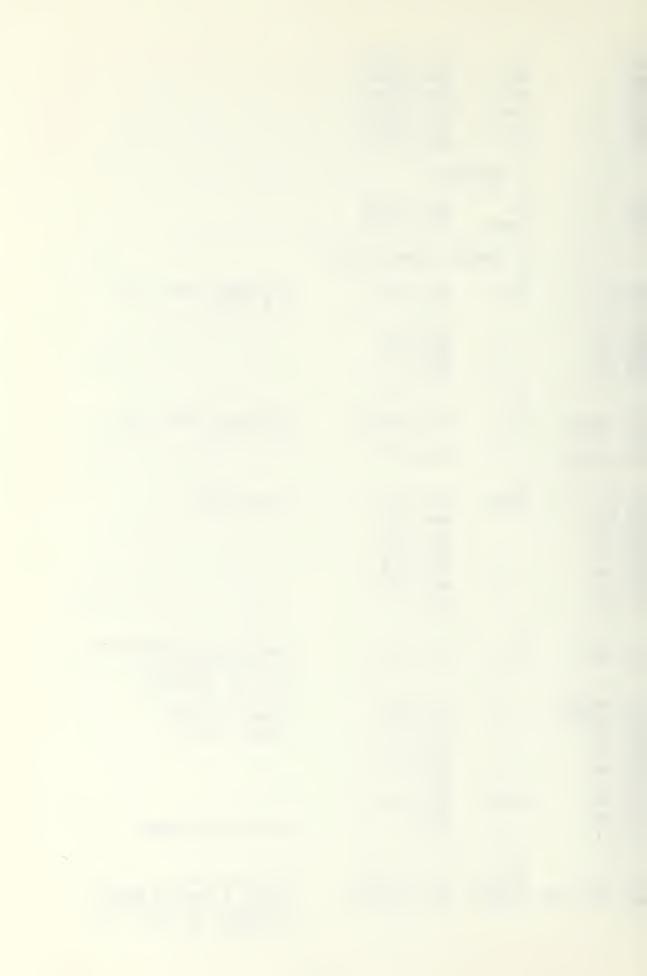
0C5E 0C62 0C65 0C69 0C6C 0C70 0C72 0C75 0C77 0C7A 0C7C 0C7F 0C83 0C80 0C80 0C80 0C80 0C90 0C94 0C97 0C9A 0C9D 0CA1 0CA4 0CA7	2E273630 46E800 2E1036B1 464500 467C00 C1 3C4E 687A0C 3E59 447C0C 3E4E 467100 2E2636A7 46E800 467100 2E1036C0 46FC00 2E1036C0 463A01 467100 467100	CNTU5: NOEXP: CNTU6:	MVI M, Y' JMP CNTUG MVI M, N'	"E" FORMAT FLAG STORE GET VALUE ECHO BACK RESTORE A JUMP IF "N" ELSE STORE "YES" ELSE STORE "NO" ASK QUESTION ENTER CHANNELS IN DESIRED ORDER IN THE SCAN BUFFER CONVERT TO BCD
			EXECUTION POINT	FOR MANUALLY SCANNING
OCB1	C7	SCAN:	LXI H,SCANB MOV A,M	;VALIDITY CHECK
	3C2A 68CAOC 467100		CPI *** JZ ERR1 CALL CRLF	;BOOT DEFAULT
OCBA	46340B	LOOP:		START SCAN ROUTINE WAIT FOR COMMAND FROM OPERATOR
	46FC00 463B0B		CALL GETD CALL RSCAN	;RESCAN FOR ANOTHER
OCCA OCCE	44B DOC 2E263610 46E800 449700	ERR1: END	JMP LOOP LXI H,LERRI CALL LIST JMP CNTRL	;SET OF DATA ;CONTINUE ;MSG OUT

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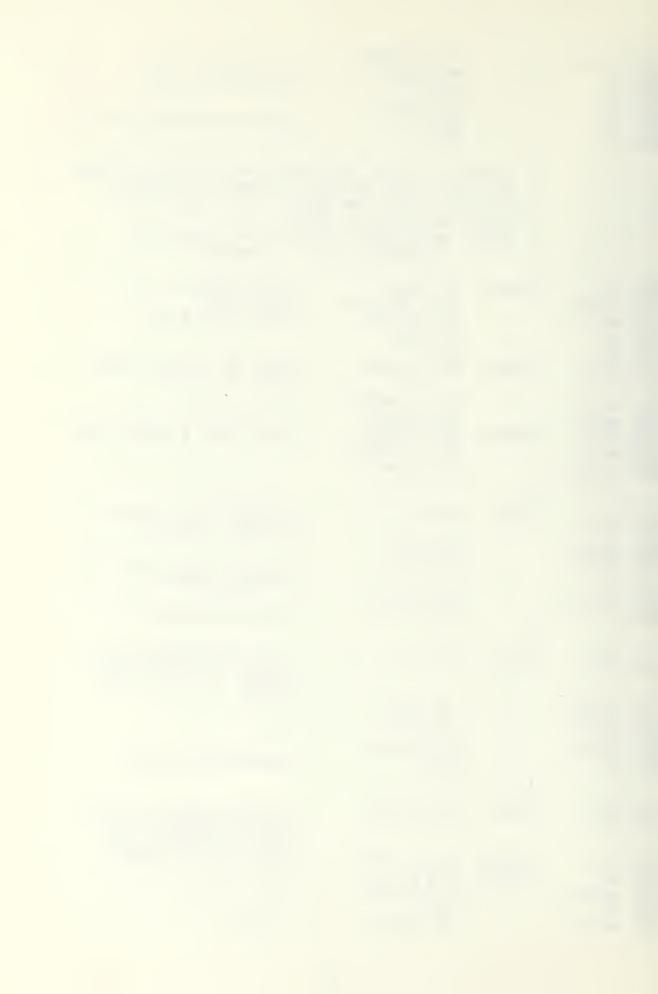


	; THIS S ; TO TUN ; ORDER ; MOVE ; LISTES ; IN THE	SECT RNO TO TO DHEI E "RI	N TWO RELAYS CAUSE SOME PF DESIRED LOCA RE ARE ALSO U UN" SECTION D	THE LOGIC NECESSARY ('UP' AND 'DOWN') IN HYSICAL DEVICE TO ATION. SUBROUTINES JSED BY THE SOFTWARE
0000		ES NO	OCEOH DT ANNOTATED US SECTIONS.	CAN BE FOUND
10A0 10 <b>A</b> 9	STACK FCNT		10A0H STACK+9	;COUNTER IN NOISE ;FILTER ROUTINE
0009 0030 0050 0010 0060	CMD CMDUP CMDDN OFF BUMP	EQU EQU		OUTPUT PORT 1 ACTIVATE "UP" RELAY ACTIVATE "DOWN" RELAY RELAYS OFF TRANSPORT DELAY
106D 0AE9	RAW DATA		106DH 0AE9H	;(SEE TEXT) ;RAW DATA INPUT BUFER ; RAW DATA TO F.P. ;LOAD ACCUM AND POINT :TO OPERAND
006A 0831	HALF VRI		006AH 0831H	;4.5 MS DELAY ;VOLT UNITS TO
081B	RVI	EQU	081BH	;BINARY A/D COUNT ;BINARY A/D COUNT TO ;VOLTAGE UNITS
1080 0B41	CFBUF CNTU7		1080H 0B41H	;CONVERSION FACTORS. ;REMOTE ENTRY TO "SCAN" ;ROUTINES WITH COLUMN ;HEADINGS OFF
1000	SCANB	EQU	ТОСОН	BUFFER CONTAINING CHANNELS TO BE SCANNED
0189 0800 000D 0065 0071 0097 00F8 013A 0145 00E8 0154 1040 1050 1058 033E 036E	FPBIN SHOLD SCHAR DELAY CRLF CNTRL GET STRIP DISPY LIST BINFP DOPND FOPND STORE STR LOD		0065H 0071H 0097H 00F8H	;BIN←F.P.

03 D7 038C 03B 4 06 4B 070F 0FE 4		AD MUL DIV INN OUU I5	EQU 03D7H EQU 038CH EQU 03B4H EQU 064BH EQU 070FH EQU 0FE4H	
		MESSA	GES	
26FF 277F		Č3 LMOVE	EQU 26FFH EQU 277FH	
		COMMO	N SUBROUTINES	
OCEO	C 4	GT:	MOV A,E	;RETURNS CARRY SET :IF BC>DE
OCE1 OCE2 OCE3 OCE4	C3 99		SUB C MOV A,D SBB B RET	;ir boyde
		, ,		
OCE5	46EBOC	LT:	CALL SWAP	;RETURNS CARRY SET :IF BC <de< td=""></de<>
0C E 8	46E00C		CALL GT	,11 00 00
OCEB OCEC OCED OCEE OCEF OCFO OCF1	E2 D0 C3 D9	; SWAP:	MOV A,E MOV E,C MOV C,A MOV A,D MOV D,B MOV B,A RET	;DE←BC←DE
OCF2	3601	,	MVI L,01	;ABS(ACTUAL-DESIRED) ;L=1 IF ACTUAL>
OCF7 OCFA OCFB OCFC OCFD	C2 94 07	ι	CALL GT JNC YES DCR L MOV A,C SUB E RET	;L=O IF ACTUAL< ;IS DE>=BC ? ;JUMP IF TRUE ;RESET FLAG
OCFE OCFF ODOO	92	YES:	MOV A,E SUB C RET	;ACTUAL>DESIRED
0D01 0D03	0610 2E10366E	DATA1: DATA2:	MVI A,OFF LXI H,RAW+1	;RELAYS OFF ;TAKE A SAMPLE FROM ;CHANNEL O. RELAY DIR ;ASSUMED IN A



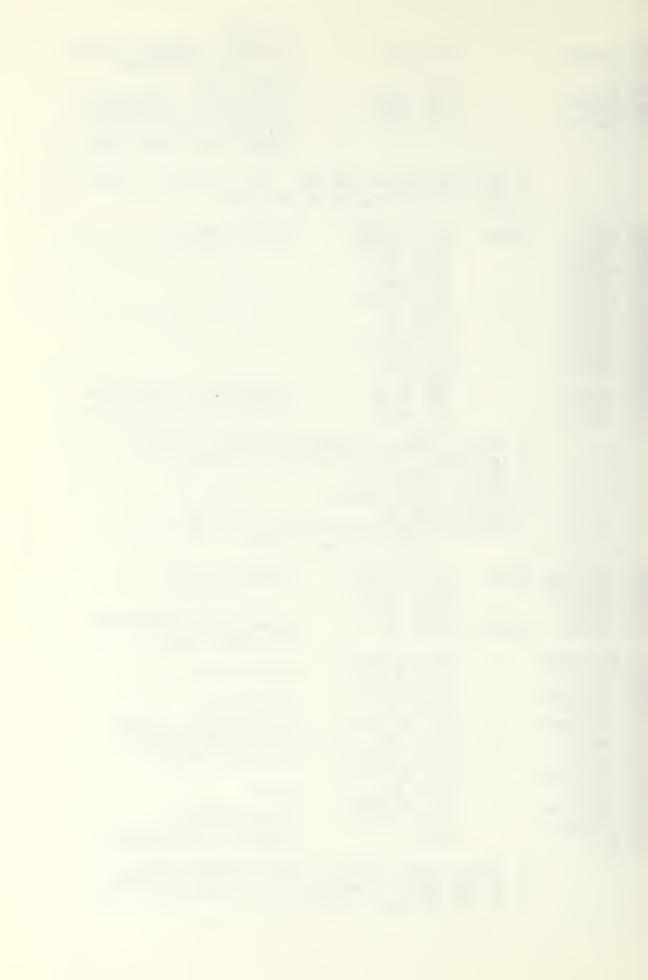
0D07 0D0A 0D0B 0D0C 0D0D 0D0E	30 E7 31		MOV INR MOV	E,M L	;DE+SAMPLED DATA ;POINT TO SAMPLED DATA
		OF KN EXTER KNOWN	OWN NAL AMO : A=	TIME DURATION DEVICE MOVEMN UNT. (SEE TE)	US RELAY DIRECTION
0D10 0D13 0D15 0D16	07		CAL MVI OUT RET	CMD L DELAY+2 A,OFF CMD	;TURN RELAYS OFF
	1E60	BUMPU:		D,BUMP	;MOVE UP A SHORT DIST ;BUMP = DELAY
0D1B 0D1E 0D20	0630 440F0D 1E60 0650 440F0D	BUMPD:	JMP MVI MVI	A,CMDUP BUMPM D,BUMP A,CMDDN BUMPM	;MOVE DOWN A SHORT DIST
0.025	3006	, ADJU:	CPI	6	;IF LOCATION ERROR >5
	40980D		JNC		;COUNTS, MOVE UP
	3003		CPI		;IF LOCATION ERROR > 2 ;COUNTS, BUMP UP
0 D2C 0 D2F	40170D 07	;	JNC RET	BUMPU	;CONVERGENCE EXIT
0D30	3006	; ADJD:	CPI	6	;ERROR ADJUSTMENT (AS ;ABOVE) FOR THE DOWN ;RELAY
0D35			CPI		,
0D37 0D3A	401EOD 07	;	JNC RET	BUMPD	;CONVERGENCE EXIT
OD3B	2E64	; Long:	MVI	H,100	1.0 SEC DELAY TO GIVE TIME FOR RELAY AND DRIVE MOTOR TO STOP
0D43	31 483F0D	LOOPA: LOOPB:	DCR JNZ DCR	LOOPB	; INNER LOOP COUNTER



0D47	07		RET	
		; THE A ; OF NO ; EXTER ; ABSOL	/D CONVERTER AND ISE WHICH COULD NALLY CONTROLLED	TER OUT "GLITCHES" FROM TO MINIMIZE THE EFFECT MAKE THE DEVICE STOP WITH AN L-DESIRED) GREATER THAN
0D4C 0D4E 0D51 0D54	3E7F 46010D 466A00 46ECOA		LXI H,FCNT MVI M,127 CALL DATA1 CALL HALF CALL DATA+3 CALL STR CALL DATA1	<pre>;N=COUNT=127 ;STOP AND TAKE A SAMPLE ;RELAY REACTION TIME ;CONVERT/LOAD/POINT ;M←A(1) ;TAKE 127 MORE SAMPLES ;FORMING RUNNING SUM</pre>
0D60 0D63 0D67 0D6A 0D6E 0D6F 0D70 0D71 0D74 0D78 0D78 0D75 0D82 0D86 0D89 0D8 0	463E03 2E1036A9 CF 09 F9 485A0D 2E103650 466E03 2E0F36E4 468C03	FAVE:	MOV M,B JNZ LOOP LXI H,FOPND CALL LOD LXI H,I5 CALL MUL LXI H,FOPND CALL STR LXI H,FOPND	; A(N)←A(N)+M ; M←A(N)
0D94 0D95 0D96 0D97	30 D7	;	MOV B,M INR L Mov C,M Ret	ATE THEIR RESPECTIVE
		; RELAYS	S IN ORDER TO DRI ERO' (SEE TEXT)	VE POSITION ERROR
0 D 9 A 0 D 9 D 0 D A 0	0630 46030D 46E00C 60980D 46480D			;A←UP COMMAND ;TAKE SAMPLE ;IF ACTUAL <desired, up<br="">;ELSE TURN OFF RELAY ;AND CHECK FOR NOISE</desired,>
ODA9	46E00C 60980D 463B0D		CALL GT JC UP CALL LONG	; IF UNDERSHOOT, JUMP ; WAIT FOR DRIVE MOTOR



ODB2 ODB3	46F20C 31 48250D 44300D	\$	CALL AB DCR L JNZ ADJU JMP ADJD	TO STOP COMPUTE ABSOLUTE VALUE OF POSITION ERROR TEST FLAG UNDERSHOOT CORRECTION OVERSHOOT CORRECTION EITHER ADJU OR ADJD TESTS FOR CONVERGENCE
			NNOTATION FOR THE TO THE FOLLOWING	E "UP" ROUTINE APPLIES ROUTINE.
ODBB ODC1 ODC4 ODC7 ODCA ODCD ODC0 ODD0 ODD3			MVI A,CMDDN CALL DATA2 CALL LT JC DOWN CALL FLTR CALL LT JC DOWN CALL LONG CALL AB DCR L	;DOWN CONTROL
	48250D 44300D		JMP ADJU	;OVERSHOOT CORRECTION ;UNDERSHOOT CORRECTION
		AND RE DEVICE INPUT REGIST	" SENDS A MESSAGE EADS IN THE DESIF E POSITION. : UNRESTRICTED [ERS: ALL [: DESIRED POSITI IN A/D BINARY	ON IS IN DE
ODDE	2E27367F 46E800 467100		LXI H,LMOVE Call list Call Crlf	;MESSAGE OUT
				;IN←DESIRED POSITION ;IN USER UNITS
ODEB	2E103640 463A01		LXI H,DOPND CALL STRIP LXI H,DOPND	;BCD←ASCII
0DF2 0DF5 0DF9 0DFC 0DFF 0E03 0E06	2E 103640 464B06 2E 103680 46B403 463808 2E 103640 463E03 2E 103640		CALL INN LXI H,CFBUF CALL DIV CALL VRI+7 LXI H,DOPND CALL STR LXI H,DOPND CALL FPBIN	;F.P.↔BCD ;CONVERSION FACTOR ;VOLTS↔USER UNITS ;ABSOLUTE↔VOLTS ;M←A ;BINARY←F.P.
OEOD	468901 07		RET	BACK TO CONTROLLER
		; OF THE	" AND "CNTUB" AR ABOVE SUBROUTIN EXTERNAL DEVICE	E FOR MANUAL CONTROL



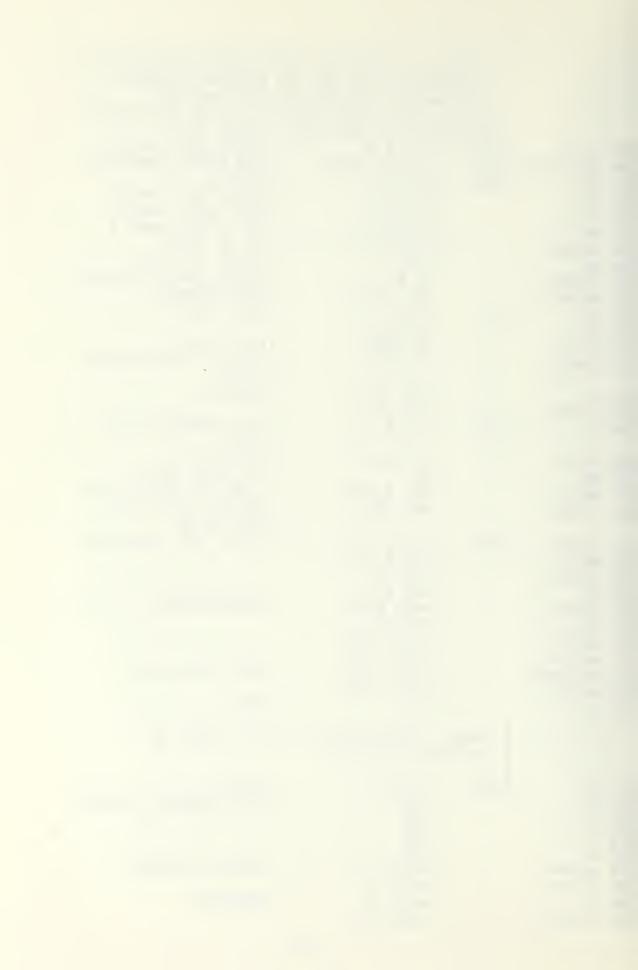
		;		
0E0E 0E12 0E13 0E14	FB 30	ĆNTUA:	LXI H,STACK+7 MOV M,D INR L MOV M,E	;M←BINARY
	46480D			TAKE A SAMPLE TO DETERMINE DRIVE DIRECTION. (UP OR DOWN)
	46E00C 603D0E		CALL GT JC MOVEU	;TURN ON "UP" RELAY
	46E50C		CALL LT	;TURN ON "DOWN" RELAY ;ELSE DO NOT MOVE
0E24		CNTUB:	JC MOVED LXI H,C3	e 9 .9 9
0E28 0E2B	46E800 2E1036C0		CALL LIST LXI H,SCANB	;LOAD CHANNEL O IN
OE2F			MVI M,O	;SCAN BUFFER
0E31 0E32			INR L MVI M,SCHAR	STOP AFTER 1 CHANNEL
0E34	46410B		CALL CNTU7	;128 POINT AVERAGE ;WITH PREVIOUSLY
				DEFINED DELAY AND FORMAT. THEN PRINT ACTUAL POSITION
	467100 444F0E		CALL CRLF JMP MANCI	
			J" AND "MOVED" AR	
			POINTS TO THE "U DL SUBROUTINES. (	
	46980D 44240E	MOVEU:	CALL UP JMP CNTUB	
0E43	46B90D 44240E	MOVED:	CALL DOWN JMP CNTUB	
		; OPERAT	' IS THE ENTRY PO OR FOR MANUAL AC ROCESSOR CONTROL	TUATION OF SOME
	46DA0D 440E0E	MANC:	CALL MOVE JMP CNTUA	
DE 4F	46E40D 440E0E	MANC1:	CALL MLOOP JMP CNTUA	
0000		END	en ensen	



DIAGNOSTICS "DUMP" IS USED TO DISPLAY THE CONTENTS OF THE CONVERSION FACTOR BUFFER. "TEST" CHECKS ALL RAM BETWEEN 1000H AND 1FFFH BY WRITING OUT A BYTE TO EACH LOCATION, READING IT BACK, AND COMPARING TO THE ORIGINAL VALUE. IF AN ERROR IS DECTECTED, THE TTY BELL IS RUNG AND A MESSAGE IS PRINTED OUT ALONG WITH THE CONTENTS OF THE BAD MEMORY LOCATION AND IT'S ADDRESS. OCG OE 60H ; EQUATES NOT ANNOTATED CAN BE FOUND							
		•	EVIOUS SECTIONS.				
0080		ĊFB	EQU 080H	;LOW ADD OF CONVERSION ;FACTOR BUFFER			
00A0 007C 10A0 0071 00E8 0145 0097 1070 036E 070F		CO STACK CRLF LIST DISPY CNTRL RESLT LOD OUU ;	EQU 10A0H EQU 0071H EQU 00E8H EQU 0145H EQU 0097H EQU 1070H EQU 036EH EQU 070FH	LOW ADDRESS OF STACK			
		MESSAGES					
27A5		ŔAM •	EQU 27A5H				
0E64 0E66 0E67 0E6A	2E1036A0 3E80 F7 466E03 2E103670 460F07	DLOOP:	LXI H,STACK MVI M,CFB MOV L,M CALL LOD LXI H,RESLT CALL OUU	;STARTING LOCATION ;GET VECTOR ;A←CONVERSION FACTOR ;DUMP TO OUTPUT			
	464501		CALL DISPY	;BUFFER ;ASCII←BCD			
0E74 0E78 0E78 0E7E 0E82 0E83 0E85 0E85 0E86 0E88	2E103670 46E800 467100 2E1036A0 C7 0404		LXI H,RESLT CALL LIST CALL CRLF LXI H,STACK MOV A,M ADI 4 MOV M,A	PRINTOUT INFO NEW LINE FETCH VECTOR A←A+4 SAVE VECTOR CHECK FOR LAST IF TRUE, DONE ELSE GET NEXT			
		, , ,					



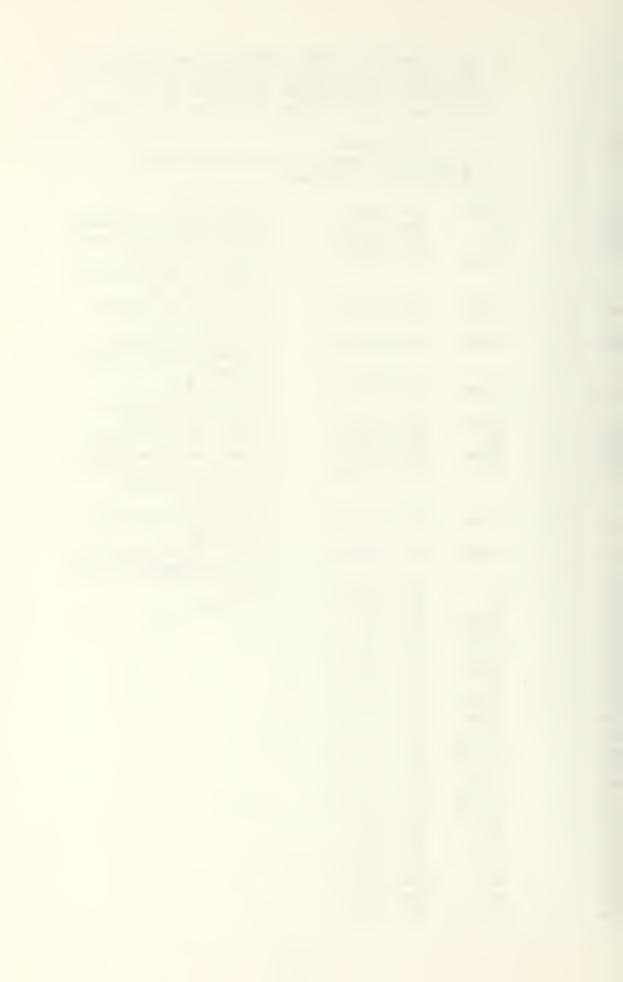
OEAA	44940E	; TO ENS ; ALTERN ; 1'S TO ; READ H MTEST: NEXT: LOOP: PC:	SURE IT IS ALL I NATELY WRITES ALL D EACH CELL AND BACK IS THE ONE MVI A,O LXI H,1000H MOV B,A MOV M,A MOV A,M CMP B JNZ ERR3 INR L JZ PC JMP LOOP INR H MOV A,H CPI 20H JZ NEW MOV A,B JMP LOOP	TESTS THAT THE VALUE IT SENT OUT. ;FIRST TEST VALUE ;RAM START LOCATION ;SAVE A ;WRITE TEST VALUE ;READ TEST VALUE ;IS IT THE SAME ? ;IF NO JUMP ;POINT TO NEXT ;CHECK PAGE CROSSING ;TRY ANOTHER ;NEXT PAGE ;LAST PAGE ? ;IF YES, CHECK LAST ;VALUE ;RESTORE
OEAE OEBO OEB 1	C1 3CFF 2B 06FF 44900E		CPI OFFH	RETEST RAM WITH
0EB9 0EBB 0EBE 0EBF 0EC2 0EC3 0EC6 0EC9	0E2F 467C00 C5 46D10E C6 46D10E 467100 2E2736A5 46E800	; "HEXT"	CALL LIST RET ' OUTPUTS HEX DAT	;ADDRESS OUT ;TELL OPERATOR ;DONE
OED9 OEDC	1A 1A 1A 1A 46E40E 467C00	; HEXT:	RAR RAR RAR RAR	;SAVE ;LO NIBBLE←HI NIBBLE ;OUTPUT ROUTINE ;RESTORE



OEE6 3COA CPI 10 ;IS IT A OEE8 60EDOE JC NUM ;IF TRUE,	, JUMP NSTRUCT LETTER IAS
---	---------------------------------



	; THE "I ; AUTOM	MOVE ATIC	" AND "SCAN" ,INCREMENTAL	SECTION MAKES USE OF SECTIONS TO PROVIDE STEPPING OF AN EN ARBITRARY LIMITS.
0000		ES NO	2000H DT ANNOTATED US SECTIONS	CAN BE FOUND
OCEB OCEO OCE5 ODE4	ŚWAP GT LT MLOOP	EQU EQU	OCEOH	;DE←BC←DE ;DE-BC,RES NOT SAVED ;BC-DE,RES NOT SAVED ;FETCH EXTERNAL ;POSIT,CONVERT, AND ;STORE IN DE
0D48	FLTR	EQU	OD48H	GLITCH AND NOISE
0B3B	RSCAN	EQU	овзвн	;FILTER ;RE-SCAN DESIRED ;CHANNELS AND PRINT ;RESULTS
0B34	SCAN5	EQU	0B34H	PRINT OUT COLUMN HEADINGS
OCCA OD98 ODB9 1060		EQU EQU	ОССАН ОД98Н ОДВ9Н 1060Н	TERMINAL ERROR TURN ON UP DRIVE TURN ON DOWN DRIVE ITERATION COUNTER FLOATING POINT STORAGE
1064	TEMP	EQU	1064H	TEMPORARY PRODUCT
1068	FINC	EQU	1068H	F.P. REPRESENTATION OF INCREMENTAL DIST
OFEO OFFO 000D 0071 00E8 0097 00F8 013A 0154 0189 1040 1058 10A0 1058 10A0 10C0 1080 0080 033E 036E 03D7 038C 03B4		EQU EQU EQU EQU EQU EQU EQU	OFEOH OFFOH ODH OO71H OOE8H OO97H OOF8H O13AH O154H O154H O154H I040H I058H I040H I058H I0A0H I058H I0A0H I058H 030H O33EH O38CH O38CH O38 AH	; OF INCREMENTAL DIST ; 1.0 ; 819.15



064B		INN	EQU	064BH	
		MESSA	GES		
2672 2746 2755 2764 2772		LREAD START STOP INCRE LUNIT	EQU EQU	2746H 2755H	
		; COMMO	N SU	BROUTINES	
		; TRANS	FER	ND "STRXX" A OF DATA BETW AND MEMORY.	RE USED FOR 2 WAY EEN CPU
2000	2E1036A7	STRD:	LXI	H,STACK+7	;CURRENT DESIRED :POSITION STORAGE
2004 2005 2006 2007	FA	MBC:	INR		, ODITION DIONAGE
	2E1036A7 CF 30 D7	LODD: BCM:	LXI MOV INR	H,STACK+7 B,M L C,M	;BC←M
2010		STRST:	LXI	H,STACK+18 MBC	;START POSITION :M←BC
2017		LODST:	LXI	H,STACK+18 BCM	
201E 2022 2023 2024 2025	2E1036B4 FB 30 FC	STRS: MDE:	IXI	H,STACK+20 M,D L M,E	;STOP POSITION
	2E1036B4 DF 30 E7	LODS: DEM:		H,STACK+20 D,M L	;DE←M
202E	2E1036B6 442220	STRI:			;INCREMENTAL POSITION
2035	2E1036B6 442A20	LODI:		H,STACK+22	;DE←M
203C	2E103660	; INCN:	LXI		;INCREMENT ITERATION ;COUNTER (N)
2043	466E03 2E0F36E0		LXI	H,I6	.; A←N
204A	46D703 2E103660		CALL	H,N	; A = A+1
204E	463E03		CALL	. STR	; N←A

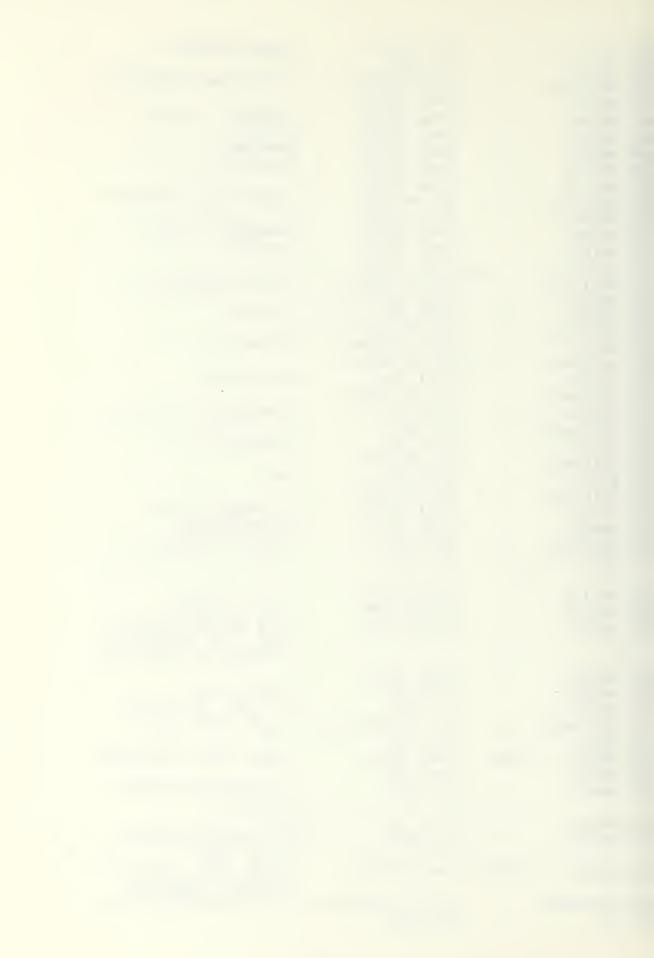


2055 2058 205C 205F 2063 2066	463E03 2E103664 468901 462E20		LXI H,FINC CALL MUL LXI H,TEMP CALL STR LXI H,TEMP CALL FPBIN CALL STRI CALL LODI	;I←N*I ;SAVE FACTOR ;BINARY←F.P. ;M←NEW INCREMENT ;LOAD START POSIT
206F	441720 460020 442620	STOR:	JMP LODST Call Strd JMP LODS	;AND INCREMENT ;STORE NEXT POSIT ;LOAD STOP POSIT
		; "INCP ; DECRE ; BY "I ; THE R ; BC←S ; STAC	MENT THE VALUE OF NCREMENT" AMOUNT. OUTINE RETURNS WI TART POSIT+INCREM	TH:
2075 2078	463C20 C4	INCP:	CALL INCN MOV A,E	;INPUT PARAMETERS ;ADD BC+DE AND STORE ;RESULT IN BC
	DO C3 89 C8 446F20 463C20		ADD C MOV C,A MOV A,D ADC B MOV B,A JMP STOR CALL INCN MOV A,C	;EXIT THROUGH STORE ;INPUT PARAMETERS ;SUB DE-BC AND STORE
2085 2086 2087 2088 2089 2089	DO C1 9B	;	SUB E MOV C,A MOV A,B SBB D MOV B,A JMP STOR	;RESULT IN BC ;EXIT
2091	2E1036B8 3E01 441B21		LXI H,STACK +24 MVI M,1 JMP RUNL	;SET SUBT FLAG ;BACK TO "RUN LOOP"
		; TO THE ; PROVID ; 8 CHAN	IS THE OPERATOR E AUTOMATIC CONTR DES REPEATED SCAN INELS AT SELECTED NAL DEVICE.	OL ROUTINE. IT NING OF UP TO
2096 209A	2E1036C0 C7	RUN:	LXI H,SCANB MOV A,M	;VALIDITY CHECK



	3C2A		CPI ***	;BOOT DEFAULT
20A0	68CAOC 2E273646		JZ ERRI LXI H,START	;GET START POSITION
20A7	46E800 46E40D		CALL LIST CALL MLOOP	CONVERT TO BIN
20AD	46EB0C 460020		CALL SWAP Call strd	;BC←DE ;M←START
	461020 467100		CALL STRST CALL CRLF	;M+START
20B6	2E273655 46E800		LXI H,STOP CALL LIST	;GET FINAL POSITION
20B D	46E40D		CALL MLOOP	;CONVERT TO BIN
2003	461E20 467100		CALL STRS CALL CRLF	;M←STOP
	2E273664 46E800		LXI H,INCRE CALL LIST	;GET INCREMENT
20C D	46F800 2E103640		CALL GET LXI H,DOPND	;INPUT←I
20D4	463A01		CALL STRIP	;BCD←ASCII
20 D B	2E103640 464B06		LXI H, DOPND CALL INN	;F.P.←BCD
20E2	2E103680 46B403		LXI H,CFBUF Call div	;I+VOLT(I)
	2E0F36F0 468C03		LXI H,II Call Mul	;I+BIN(I)
20EC	2E103668 463E03		LXI H,FINC CALL STR	;M←I
20F3	2E103668		LXI H,FINC	
	468901 462E20		CALL FPBIN CALL STRI	;DE←ABSOLUTE(M) ;M←INCREMENT
20FD 2101	2E103660 3F00		LXI H,N MVI M,O	RESET COUNTER
	467100	<b>,</b>	CALL CRLF	
2106	46340B		CALL SCAN5	;COL HEADINGS
2109	460820		CALL LODD	GET START AND STOP
2100	462620		CALL LODS	;INCREMENT DIRECTION
210F	46E00C		CALL GT	START>STOP ?
2115	608D20 2E1036B8		LXI H,STACK+24	;IF YES, SET FLAG ;ELSE RESET FLAG
2119 2118	3E00 46480D	RUNL:	MVI M,O Call Fltr	TAKE POSIT READINGS
211E	46E00C 604C21		CALL GT JC INCR	
				;MOVE UP
	46E50C 605221		CALL LT JC DECR	;IF DESIRED <actual,< td=""></actual,<>
212A	463B0B	CNTUC:	CALL RSCAN	;MOVE DOWN ;ELSE TAKE A SET OF
	2E1036B8			CHANNEL READINGS
2131			MOV A,M	,

.



	3C01 684321		CPI 1 JZ DECR1	;IF SET, DECREASE
	467520	INCR 1 .	CALL INCP	;POSIT BY "I"
213A	46E00C		CALL GT	
213D	605821	TEST:	JC EXIT	;IF STOP POSIT ;EXCEEDED, EXIT
	441B21 468120	DECRI.	JMP RUNL CALL DECP	ELSE REPEAT
2146	46E50C	DEGRI.	CALL LT	
2149	443D21		JMP TEST	;CHECK FOR STOP ;POSIT EXCEEDED
	46980D 442A21	INCR:	CALL UP JMP CNTUC	MOVE UP
2152	46B90D	DECR:	CALL DOWN	;MOVE DOWN
2155	442A21 467100	EXIT:	JMP CNTUC Call Crlf	
215B	467100 449700		CALL CRLF JMP CNTRL	
2156	449700	;		
		; "UNIT"	' IS USED TO INPU	JT CONVERSION FACTORS
		; WHICH	CHANGE THE INTER	RNAL UNITS (VOLTS)
		; OPERAT	ION IS THEN IN T	THE USER. ALL I/O TERMS OF THESE
		; NEW UN	ITS UNTIL RESET.	,
	2E263672 46E800		LXI H,LREAD Call list	;"CHANNEL = ?"
2168	46F800		CALL GET LXI H,DOPND	;INPUT CHANNEL
2161	463A01		CALL STRIP	;BCD+ASCII
2172 2176	2E103640 C7		LXI H,DOPND MOV A,M	:GET CHANNEL
2177	BO		ORA A	CLEAR CARRY MPY BY 4
2178	12 12		RAL RAL	
	0480 2E1036A6		ADI CFB LXI H,STACK+6	;COMPUTE VECTOR
2180	F8		MOV M,A	;STORE IT
	467100 2E273672		CALL CRLF LXI H,LUNIT	;"UNIT/VOLT =?"
	46E800 46F800		CALL LIST CALL GET	:GET CONVERSION FACTOR
218E	2E103640		LXI H,DOPND	
	463A01 2E103640		CALL STRIP LXI H,DOPND	;BCD←ASCII
2199	464B06		CALL ÍNN LXI H,STACK+6	;F.P.←BCD •GFT_VECTOR
21A0			MOV L,M	;POINT TO STORAGE
	463E03 467100		CALL STR CALL CRLF	;M←FACTOR
21A7	446121		JMP UNIT	;GET NEXT
0000		END		

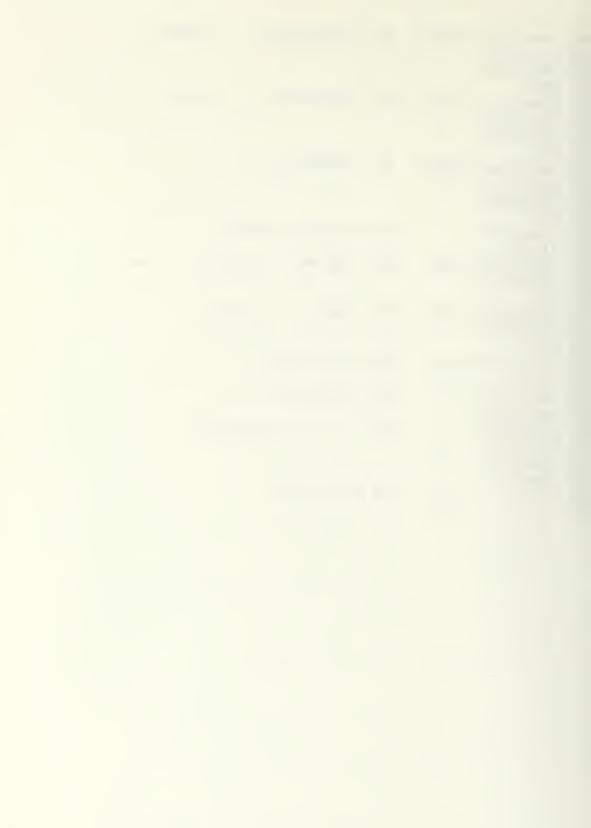


; ALL MESSAGES USED BY THE SYSTEM ARE : CONTAINTED IN THIS SECTION. ; ORG 2600H 0000 SCHAR STOP CHARACTER EQU ODH 000D LF EQU OAH :END OF RECORD **A000** READY: DB '> ',SCHAR 2600 3E200D 2603 204E4F54 LERR: DB 'NOT DEFINED', SCHAR 2607 20444546 260B 494E4544 260F 0D 2610 30313A20 LERRI: DB '01: CHANNELS ' 2614 4348414E 2618 4E454C53 2610 20 261D 4E4F5420 DB 'NOT DEFINED' SCHAR 2621 44454649 2625 4E45440D 2629 30323A20 LERR2: DB '02: INVALID ' 262D 494E5641 2631 40494420 DB "FILE" 2635 22464940 2639 452220 DB 'TERMINATION'.SCHAR 263C 5445524D 2640 494E4154 2644 494F4E0D 2648 2A2A2A2O LBOOT: DB '\*\*\* RESET: ALL ' 264C 52455345 2650 543A2041 2654 4C4C20 DB CHANNEL I/O IN ' 2657 4348414E 265B 4E454C20 265F 492F4F20 2663 494E20 DB "VOLTS" \*\*\* .SCHAR 2666 22564F4C 266A 54532220 266E 2A2A2A0D 2672 20434841 LREAD: DB ' CHANNEL = ',SCHAR 2676 4E4E454C 267A 203D200D 267E 56414C49 LWAIT: DB 'VALID FACTORS: '.SCHAR 2682 44204641 2686 43544F52 268A 533A200D DB 'A = 3MS',SCHAR 268E 41203D20 2692 334D530D DB 'B =15MS',SCHAR 2696 42203D31 269A 354D530D 269E 43203D32 DB 'C =25MS', LF, SCHAR 26A2 354D530A CAG OD

54204348		DB	'INPUT CHANNELS'
		DB	' IN DESIRED '
		DB	ORDER ', SCHAR
	THE		
		DB	WHEN READY TO "
54414B45		DB	TAKE DATA.
		00	· · · · · · · · · · · · · · · · · · ·
A10020			
54595045		DB	'TYPE SCAN '
414E20			
4F522052		DB	OR RUN ,SCHAR
		20	
		סט	UN. ,SUNAR
		DB	• • SCHAR
OD		00	,
	DWAIT:	DB	DELAY BETWEEN '
59204245			
		20	
		DB	DATA POINTS = '
		DB	'15 MS (DEFAULT)',SCHAR
		22	17 No (201 No21) younn
45464155			
4C54290D			
22452220	EXP:	DB	"E" FORMAT
		~ ~	
2859204F		DB	'(Y OR N) ? ',SCHAR
53544152	START.	nB	START POSIT - SCHAR
5420504F	STRUT.		START / USTI - ,BURAR
53495420			
3 02000			
53544F50	STOP:	DB	"STOP POSIT = ",SCHAR
20504F53			
49542020			
302000			
	54204348 414E4E45 4C53 20494E20 44455349 52454420 4F524445 520D 5748454E 20524541 44592054 4F20 54414B45 20444154 412C20 54595045 20205343 414E20 4F522052 554E200D 20202043 482E200D 20202043 482E200D 20202020 0D 44454C41 59204245 54574545 4E20 44415441 20504F49 4E545320 3D20 3135204D 53202844 45464155 4C54290D 22452220 464F524D 4154 2859204F 52204E29 203F200D 53544152 5420504F	54204348 414E4E45 4C53 20494E20 44455349 52454420 4F524445 520D 5748454E INFO: 20524541 44592054 4F20 54414B45 20444154 412C20 54595045 20205343 414E20 4F522052 554E200D 20202043 C2: 482E200D 20202020 C3: 0D 44454C41 DWAIT: 59204245 54574545 4E20 44415441 20504F49 4E545320 3D20 3135204D 53202844 45464155 4C54290D 22452220 EXP: 464F524D 4154 2859204F 52204E29 203F200D 53544152 START: 5420504F 53495420 3D200 53544F50 STOP: 20504F53 49542020	41 4E 4E 45         4C53         20 49 4E 20       DB         44 4553 49       52454420         4F524445       DB         520D       5748 454E       INFO:       DB         20524541       44592054       44592054       44592054         4F20       5441 4B 45       DB       2020205343         412C20       54595045       DB       20205343         414E20       4522052       DB       2020203200D       C1:       DB         20202020       C3:       DB       20202020D       C3:       DB         20202020       C3:       DB       20202020D       C3:       DB         20202020       C3:       DB       20202020       C3:       DB         20204245       54574545       4E20       44415441       DB         20504F49       4E545220       208       3135204D       DB         532028444       45464155       4C

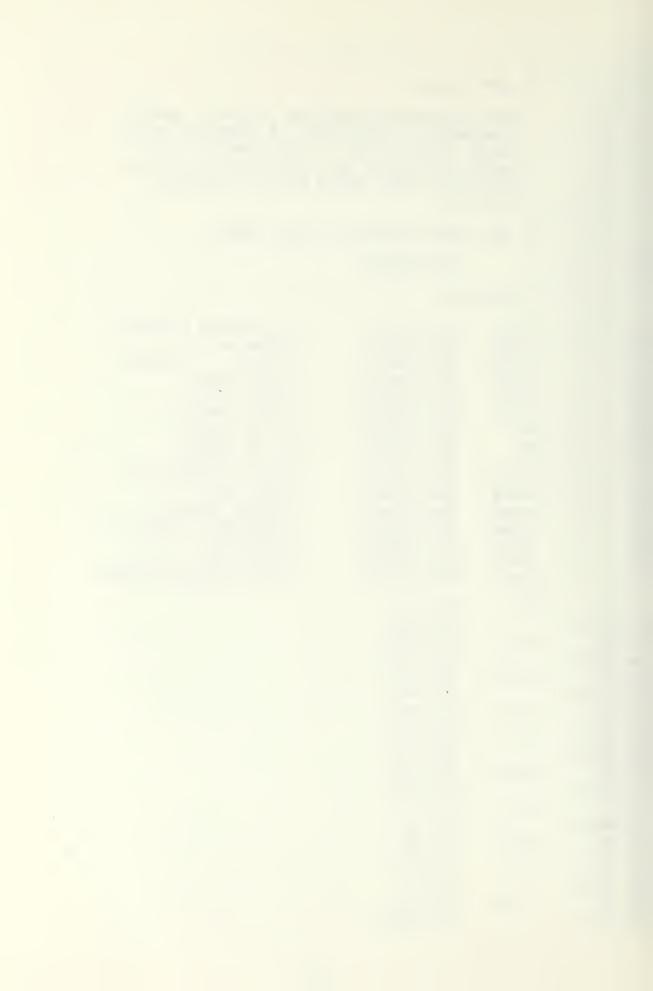


2764 494E4352 INCRE: DB 'INCREMENT = ',SCHAR 2768 454D454E 276C 5420203D 2770 200D 2772 554E4954 LUNIT: DB 'UNIT/VOLT = '.SCHAR 2776 2F564F4C 277A 54203D20 277E OD 277F 44455349 LMOVE: DB 'DESIRED .... 2783 52454420 2787 2E2E2E2E 278B 20 DB 'ACTUAL', SCHAR 278C 41435455 2790 414COD 2793 52554E20 LRUN: DB 'RUN NO. '.SCHAR 2797 4E4F2E20 279B OD 279C 2A2A2A2A LCOM: DB \*\*\*\*\* '.SCHAR 27A0 2A202020 27A4 OD DB O7H, 'DATA/' 27A5 07444154 RAM: 27A9 412F DB 'LOCATION' 27AB 4C4F4341 27AF 54494F4E DB ..... BAD RAM' 27B3 202E2E2E 27B7 2E2E2E20 27BB 42414420 27BF 52414D DB 07H, SCHAR 27C2 070D END 0000



	; COMPA ; EVERY	ECOGNITION ROL RES THE CHECK- THIRD ENTRY I	TINE ("RECOG", OOA1H) SUM IT COMPUTES WITH N THIS TABLE. IF A FOLLOWING TWO BYTES
	; SHOW ; ROUTI	THE ENTRY POIN	IT FOR THE DESIRED
0000	•	ORG OFOOH	
	EQUAT	ES	
0847 08F0 0901 0912 095D 0963 098B 097F 0973 0C35 0CAD 0E49 0E60 0E8E 2096 2161	CNTLR CNTLC EDIT FILE WAIT A1 B1 C1 SSCAN SCAN SCAN MOVE DUMP MTEST RUN UNIT	EQU 0847H EQU 08F0H EQU 0901H EQU 0912H EQU 095DH EQU 0963H EQU 098BH EQU 0975H EQU 0973H EQU 0C35H EQU 0C4DH EQU 0E49H EQU 0E60H EQU 0E8EH EQU 2096H EQU 2161H	VOLTMETER FUNCTION "RUN NO." "***** "COMMEMT TEXT INPUT WRITE TEXT DELAY FACTOR 3 MS DELAY 15 MS DELAY 25 MS DELAY SET SCAN ROUTINE TAKE DATA MANUAL CONTROL CONVERSION FACTORS RAM CHECK AUTOMATIC CONTROL INPUT SCALE FACTORS
0F00 1C 0F01 4708 0F03 12 0F04 F008 0F06 03 0F07 0109 0F09 26 0F0A 1209 0F0A 1209 0F0C 20 0F0D 5D09 0F0F 35 0F10 6309 0F12 41 0F13 8B09 0F15 42 0F16 7F09 0F15 42 0F16 7F09 0F18 43 0F19 7309 0F18 31 0F1C 350C	; J1: J2: J3: J4: J5: J6: J7: J8: J9: J9: J10:	DB 1CH DW READ DB 12H DW CNTLR DB 03H DW CNTLC DB 26H DW EDIT DB 20H DW FILE DB 35H DW WAIT DB 41H DW A1 DB 42H DW B1 DB 43H DW C1 DB 31H DW SSCAN	

.



OFIE	25	J11:	DB	25H
OFIF	ADOC		DW	SCAN
0F21	37	J12:	DB	37H
0F22	490E		DW	MOVE
0F24	36	J13:	DB	36H
0F25	600E		DW	DUMP
0F27	8D	J14:	DB	8DH
0F28	8EOE		DW	MTEST
OF2A	F5	J15:	DB	OF 5H
OF 2B	9620		DW	RUN
OF2D	40	J16:	DB	40H
OF2E	6121		DW	UNIT
0F30	00	STOP:	DB	OOH

	; ; THE FO ; POINT	LL( REF	STORAGE DING DATA ARE PRESENTATION C DUGHOUT THE PR	OF CONSTANTS
OF31	\$	OR (	G OFEOH	
OFE0 81000000	I6:		81H,0,0,0	:1.0
OFE4 7A000000			7AH,0,0,0	;.0078125
	;			
OFE8		OR	G OFFOH	
OFFO 8A4C	I1:	DB	8AH,4CH	
OFF2 C99A		DB	OC 9H, 9AH	;819.15
OFF4 84200000	12:		84H,20H,0,0	;10.0
OFF8 8A4C	13:		8AH,4CH	
OFFA C99A			0C 9H, 9AH	;819.15
OFFC 8D7F	I4:		8DH,7FH	
OFFE FCOO		DB	OFCH,O	;8191.15
0000	END			

~

# VI. <u>RECOMMENDATIONS</u>

The ADL software was developed on an existing development system which used the following:

- 1. 110 baud teletype for program listing.
- 2. 110 baud paper tape punch for mass storage.
- 3. 1200 baud high speed paper tape reader.
- 4. 1200 baud CRT for program entry and editing.

While this system is a useful tool for writing and debugging small programs, it is not a viable system for large scale development. The percentages of time devoted to the creation of the ADL software package was 15% logic development, 5% manual entry, 15% debugging and 75% waiting for paper tape and teletype output. The last figure represents a significant and costly waste of manpower assets. The following system - while more expensive - could easily pay for itself in man-hour savings alone:

- Floppy disk mass memory. this reduces an edit-assembly-reedit-reassembly cycle from up to eight hours (for the entire package) to less than five minutes (also for the entire package).
- Line printer for producing source code and assembly listings.
- 3. Resident high level language such as BASIC or PL/M to enhance complex logic manipulations.

The Department of Aeronautics has recently acquired the

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INTEL MDS 80 development system. This system contains the above components and is presently being used as a data acquisition system for an oscillating flow wind tunnel. In addition to data logging, this system can perform on-line fast fourier analysis of data taken in a highly turbulent and non-linear environment [2].

Microprocessor usage presents a unique problem; namely, better CPUs and more advanced peripherals appear on the market almost monthly. Therefore, a U-P oriented system rapidly becomes outdated. The software for the ADL was written using industry standard techniques. A change to the more advanced 8080 CPU can therefore be accomplished (with minor changes) by simply reassembling the program with an 8080 assembler. Such an update is recommended if the ADL is to be used to take higher frequency data.

# APPENDIX A

# GLOSSARY

- 1. A/D: analog to digital (adjective or noun)
- assembly: A listing which contains both source code and machine code.
- BAUD: A data transmission rate expressed in BITs per second.
- BIT: BInary digit. A single unit of information in a binary word.
- buffer: A group of memory locations used to store specific data (input data, constants, output data, etc.).
- 6. buffering: A process by which electronic signals possessing different properties are made compatible.
- byte: An eight-BIT word which is processed as a single quantity.
- CPU: Central Processing Unit. The area of the microprocessor which computes and sequences all logic and arithmetic functions.
- 9. coordination number: A sequential, numerical label associated with a set of data points for a given run.
- CRT: Cathod Ray Tube. Also used as the generic name for a television type display.
- 11. D/A: The inverse of the A/D process.

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- 12. data logging: The acquisition and tabulation of data.
- 13. EPROM: erasable/programmable read only memory
- 14. driver: In a software context this term refers to a program used to control the actions of an external device.
- external device: A physical device which is not an integral part of the microprocessor.
- 16. glitch: A missing BIT in a byte of data which can occur during data transmission or conversion.
- 17. H: A suffix which indicates a hexadecimal number (Appendix C).
- 18. I/O: input/output
- 19. K: A suffix which indicates a group of 1024 (2 ) items as in '4K of memory' meaning 4096 memory locations.
- 20. machine code: The BIT patterns actually used by the U-P in order to carry out its assigned logic functions.
- 21. MUX: a multiplexing device
- 22. nibble: The upper or lower four BITs in one byte.
- OS: Operating System. Another term for Software Package.
- 24. page: a 256 byte segment of memory
- 25. RAM: Random access memory. Volatile memory used for variable storage and data manipulation.
- 26. register: A storage location located in the CPU.
- 27. ROM: read only memory, non-volatile
- 28. software: The program which resides in the U-P's memory.

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- 29. source code: The program written by the user.
- 30. U-P: microprocessor
- 31. 8008: An 8-BIT U-P device.
- 32. 8080: The next generation U-P from the 8008.

# APPENDIX B

VENDOR DATA

The following specification sheets give the major properties of the hardware used in the ADL system. Also presented are the I/O pin assignments for the 805 processor as well as the pin-outs for the other connecters used throughout the system.

# MPS 805 MICROPROCESSOR SYSTEM SPECIFICATIONS

### Physical

Three 4 5" by 6 5" printed circuit cards

## One 8111 (PU card

- One 8114 Input card
- One 8115 Output card
- One 8116 ROM card - One 8117 RAM card

Connector Requirement for each card 56 pin, 28 position dual read-out on 0-125 - enters

#### CPU Card includes

8008 CPU

Crystal clock Address latches, data buffers, and control decode circuits Power-on and external restart. DMA buffers

### **ROM Card includes**

One 1702A PROM (256 bytes) and eight PROM sockets Socket for card expansion circuit (up to 8 cards)

#### 4 - M.C. J. Actudes

Eight 21.32 RAM (1024 bytes) and thirty-two RAM sockets. solver or lard expansion circuit (up to 4 cards)

#### anch la 2 netiades

TT intre selector circuits addressable in groups of 8 - cket for le d'expansion circuit (up to 2 cards)

#### Outou: Card includes

. TTL output latch circuits addressable in groups of 8 Sucker for card expansion circuit (up to 6 cards).

#### Operational

CPU

Executes all of the 8008 instructions.

4 microsecond time state cycle using 8008 (MPS 1805).

2.8 microsecond time state cycle using 8008-1 (MPS 805-1).

Memory for data or program storage card expandable to any combination of ROM and RAM to 16384 words ROM, 2048 word capacity per card.

RAM, 4096 word capacity per card

#### Input and Output

hiput gates implement the INP instructions. Output latches implement the OUT instructions.

n into or External Restart

single line, synchronized interrupt on CPU card can be optionally wired for multi-level interrupt or Power-on external restart

Multi-level Interrupt. Control lines available for external interrupt such as 8118 priority interrupt card.

Power-on and external estart option. CPU starts at instruction location 0000 by wiring restart output from CPU card to Interrupt Request input

"MA (Direct Memory Access)

Data, address, and control lines are 3-state disconnected by the CPU following a HLT instruction allowing MA by peripherals. The CPU musi be interrupted to continue following a HALT

#### Electrical Requirements

Refer to individual data sneets and schematics on the 8111, 8114, 8115, 8116, and 8117 for interface and wiring.

Power Requirements for the five card set fully loaded

+VCC = t5% @ 3.3 Amp maximum (35mA per ROM, 50mA per RAM) GND 0 volts

VDD = 9 volis ±5% @ 900 mA niaximum (35 mA per ROM)

#### Hardware

Compatible with Series 8400 interface cards. Fits CR5, CR10 or CR19 card racks Use M273 power supply PROM's programmable on Series 81 programmers

#### Soltware

MPS 800 hardware is fully compatible with any 8008 software assuming I/O and interrupt can be assigned compatibly. Teletype operating system and system monitor available. Assemblers, compilers and simulators available through computer time sharing services





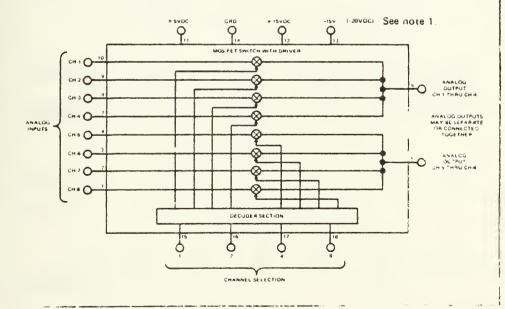
# 8 CHANNEL ANALOG MULTIPLEXER MODEL MM-8

# FOR ANALOG TIME SHARING - \$69 each

# FEATURES

- Low power consumption ..... 300 milliwatts
- □ High transfer accuracy ...... ±0.01%
- □ Fast settling output ...... 1 microsecond to ±0.01% of FS.
- Choice of input type ...... Single ended or differential
- Completely self contained ... Includes 8 MOS-FET switches, drivers and decoding logic for channel selection

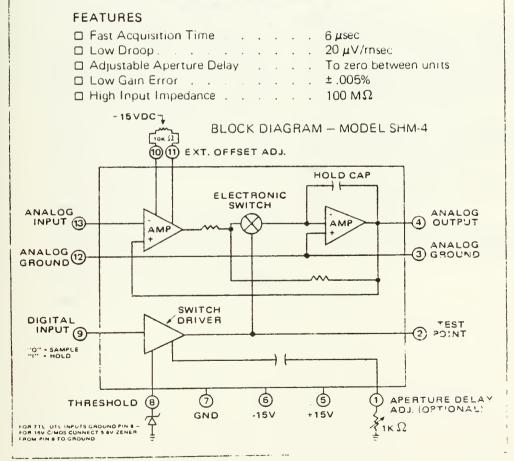
# **BLOCK DIAGRAM**







# FOR SIMULTANEOUS SAMPLE AND HOLD APPLICATIONS





#### DESCRIPTION

The SHM-4 is ideally suited to simultaneous sample and hold applications, where the gain and aperture delay between units must be matched, and where the output droop of the sampled signal is minimized for time shared A/D conversion.

A double inversion circuit in the SHM-4 places the FET sampling switch near ground, which means that all variations of hold step and of aperture delay with input voltage are eliminated.

A unique closed loop design gives high accuracy and allows the rate error to be factory nulled. Rate error is the delay by which the output lags an input ramp and may be expressed in nsec or in mV/V/µsec. For conventional sample and hold applications rate error is not serious because it merely causes an advance in the effective time of hold and tends to cancel out part of the aperture delay. However, for simultaneous applications the aperture delay minus the rate error must be matched between units so that the effective time of hold is the same for all. The SHM-4 accomplishes this by nulling the rate error to less than 1 nanosecond and for critical applications, by providing an external 5 nanosecond adjustment of aperture delay. Also, the high accuracy and low droop of the SHM-4 make it useful in conventional sample and hold applications.

Careful attention to circuit detail in eliminating leakage currents has decreased the output droop to less than 20 microvolts per millisecond allowing several SHM-4 modules to be time shared between one A/D converter.

 $^{1}$  Dynamic Accuracy of Sample and Hold Circuits, Datel Systems, Inc., Application Note V1-1.

Datel's Model MM-8 is a complete eight channel solid state analog multiplexer designed for applications which require fast output settling and high transfer accuracy.

The entire multiplexer is self contained in a plastic module measuring 0.8 cubic inches, It contains eight MOS-FET switches with associated driver circuits, each having a current limiter pull-up FET to provide minimum propagation delay, also included is all the necessary decoding logic to enable random channel addressing with a lour bit parallel binary input. Two MM-8 multiplexers can be cascaded to provide up to sixteen channels under command from one 4-BiT address. The addressing logic inputs are compatible with DTL/TTL logic levels.

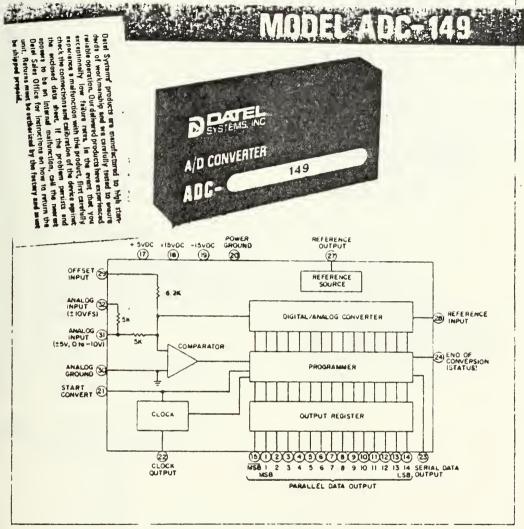
Full scale inputs can be either ±5V or ±10V with a transfer accuracy (input to output) of ±0.01%, provided the output load is a minimum of 10 megohms. The high impedance amplifier provided with Datel's ADC-E, ADC-L and ADC-M series analog/digital converters and SHM Series sample/hold's are guite suitable for this application.

Output settling time for each channel is one microsecond to  $\pm 0.01\%$  of full scale and each channel can sequentially switch at a 500 KHz rate. The output of the eight channels is divided into two parallel groups of four.

As stated before, MM-8 is complete and requires only +5VDC, +15VDC and -15VDC (-20VDC) for operation.

MM-8 modules are  $2''L \times 1''W \times 0.375''H$  in size come fully encapsulated, and feature duar in time prinning (0.100'' grid pin spacing)

# HIGH RESOLUTION ANALOG-TO-DIGITAL CONVERTER



### GENERAL DESCRIPTION

The ADC-149 is a 14 bit successive approximation type analog to digital converter for OEM use. It was specifically designed to give high resolution and accuracy at moderate cost for incorporation into precision instruments for process control systems and test and measurement systems.

The ADC-149 can resolve 1 part in 16,384 giving an operating dynamic range of 84.3dB. On the 10 volt full scale range it can detect an input change of less than 1 millivolt. Accuracy is adjustable to  $\pm.005\%$  of full scale  $\pm\%$ LSB. The temperature coefficient is held to a low  $\pm15ppm/^{\circ}C$  over the 0° to 70°C operating temperature range.

This converter accepts either unipolar or bipolar input voltages of 0 to -10V, 0 to -20V, ±5V, or ±10V full scale by external pin connection and performs a 14 bit conversion in 50 µsec. Several output codes are available including straight binary for unipolar inputs and either offset binary or two's complement for bipolar inputs. Two's complement is obtained by using the MSB output pin. Reverse coding sense is used with the most negative analog input corresponding to full scale digital output. A serial data output is also provided and has a nonreturn-to-zero (NRZ) format. Logic outputs ate DTL/TTL compatible and will drive 6 standard TTL loads

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A/D Card Code ... From P3 on Prolog Rack 22 Pin plug Luput (Command) 16 pin socket Pin Pin Ident. Ident. No. No. MUX Ch. 001 1 NC 1 2. NC 2 MUX Ch. 010 MUX Ch. 100 3 3 NC ŭ 4 S/H Command NC 5 6 5 A/D Start Ch. 0 6 Ch. 1 Alpha Relay (Incr.) 7 7 Alpha Relay (Decr.) Ch. 2 Analog 8 Relay Plug (spare) Ch. 3 8 Voltage Ch. 4 9 9 to 16 Not used Inputs Ch. 5 10 Ch. 6 11 12 Ch. 7 13 NC 14 Relay plug (spare) Relay Plug Relay plug (Alpha Relay) 15 A to P14 Spare Relay Relay plug (Alpha Relay) 16 В P17 Gnd. (Logic) 17 Relay plug (Gnd.) alpha relay (incr.) С P15 18 NC D P16 alpha relay (decr.) +5 VDC 19 Ε P21 Gnd. (Buss) 20 +15 VDC Pwr. Supply 21 Gnd. Buss line 22 -15 VDC Output (Data) 16 pin socket 1 A/D Bit 14 (LSB) 11 11 2 13 34 11 11 12 11 11 11 5 6 11 11 10 98 11 11 78 11 11 11 11 7 11 6 11 9 54 11 11 10 11 11 11 11 11 3 12 It 11 2 13 11 ŧŧ. 14 1 (MSB) 15 A/D E.O.C. 16 Spare

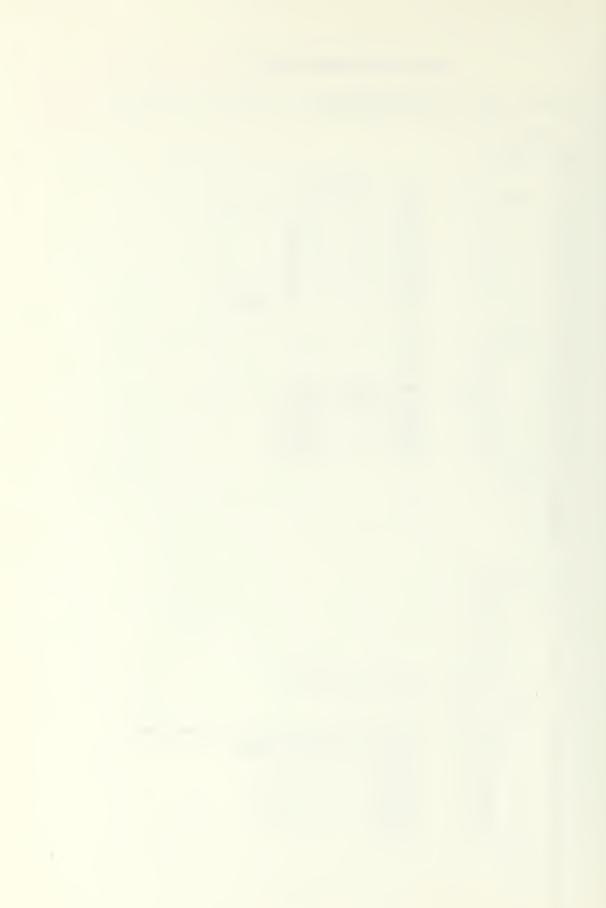


# ADL PIN CONNECTIONS

PRO	LOG SYSTEM 44 Pin Output Plug (On Top of Card Rack)
	CODE
Pin No.	Ident.
1 2 3 4 5 6 7 8 9 10 11 2 3 4 5 6 7 8 9 10 11 2 3 14 15 16 17 18 19 20 21 22	Out 1-8 to       P3-8       Relay Plug (spare)         "1-7       P3-7       Alpha Relay (decr.)         "1-6       P3-6       Alpha Relay (incr.)         "1-5       P3-5       A/D Start         "1-4       P3-4       S/H Command         "1-3       P3-3       Analog MUX Ch. 100         "1-2       P3-2       "MUX Ch. 010         "1-1       P3-1       "MUX Ch. 001
	Out 3-8 P4-15 DP (Dec. Pt., Lite Chip 03) " 3-7 P4-14 DP (Lite Chip 02) " 3-6 P4-13 DP (Lite Chip 01) " 3-5 P4-12 DP (lite Chip 00) " 3-4 P4-8 BCD Data 8 (to Lites) " 3-3 P4-7 BCD Data 4 " 3-2 P4-6 BCD Data 2 " 3-1 P4-5 BCD Data 1 NC "
A BCDEFHJKLMNPRSTUVWXYZ	Out 0-8 " 0-7 " 0-6 " 0-5 " 0-4 " 0-3 " 0-2 TTY Card JX-12/Jx-14 " 0-1 TTY Card JX-11/JX-9 NC
	Out 2-8 " 2-7 " 2-6 to P4-10 (+/- Lite) " 2-5 P4-9 Lite Enable " 2-4 P4-4 Lite MUX 1000 " 2-3 P4-3 Lite MUX 0100 " 2-2 P4-2 Lite MUX 0010 " 2-1 P4-1 Lite MUX 0001 NC "



FROL	og system <u>4</u>	4 Pin INFUT Plug (On Top of Card Rack)
C	ODE	
Pin No.	Ident.	
123456789	In 1-8 to " 1-7 " 1-6 " 1-5 " 1-5 " 1-4 " 1-3 " 1-2 " 1-1 NC	Pl-8, A/D Bit 7 Pl-7, " " 8 Pl-6, " " 9 Pl-5, " " 10 Pl-4, " " 11 Pl-3, " " 12 Pl-2, " " 13 Pl-1, " " 14 (LSB)
10 11 12 13 14 15 16 17 18 19 20 21 22	In 3-8 " 3-7 " 3-6 " 3-5 to " 3-4 " 3-3 " 3-2 " 3-1 NC " "	NC NC P2-5, Kyb'd Flag P2-4, Kyb'd (1000) P2-3, " (0100) P2-2, " (0010) P2-1, " (0001)
A B C D E F H J K L M N P R S T U V W X Y Z	In 0-8 " 0-7 " 0-6 " 0-5 " 0-4 " 0-3 " 0-2 " 0-1 to NC In 2-8 to " 2-7 " 2-6 " 2-5 " 2-4 " 2-3 " 2-2 " 2-1 NC " "	TTY Card (JX-17) Pl-16 NC Pl-15, A/D EOC (end of conversion) Pl-14, A/D Bit 1 (MSB) Pl-13, " " 2 Pl-12, " " 3 Pl-11, " " 4 Pl-10, " " 5 Pl-09, " " 6



## APPENDIX C

### MATHEMATICS PACKAGE

Floating point (F.P.) binary numbers are used internally for most internal arithmetic functions. The method is fully explained in the following excerpts from the INTEL Users Library [3].

BOOR BINARY FLOATING POINT SYSTEM

ARITHMETIC AND UTILITY PACKAGE

THE APITHMETIC AND UTILITY SUBROUTINE PACKAGE OF THE 8008 BINARY FLOATING POINT SYSTEM CONTAINS SUBPOUTINES FOR PERFORMING THE BASIC ARITHMETIC AND UTILITY OPERATIONS AVAILABLE IN THE SYSTEM.

THE APITHMETIC AND UTILITY PACKAGE IS CONTAINED IN 768 CONSECUTIVE WORDS OF MEMORY(3 BANKS OF ROM) AND DOES NOT REQUIRE THAT ANY OTHER SOFTWARE BE PRESENT IN MEMORY. THIS PACKAGE USES THE FIRST 54 WORDS OF A RANK OF RAM AS SCRATCHPAD MEMORY.

THE INDIVIDUAL SUBROUTINES INCLUDED. IN THE ARITHMETIC AND UTILITY PACKAGE OF THE FLOATING POINT SYSTEM ARE DESCRIBED IN DETAIL BELOW.

#### 8008 BINARY FLOATING POINT SYSTEM

THE 8008 BINARY FLOATING POINT SYSTEM CONSISTS OF A SET OF SURROUTINES DESIGNED TO PERFORM OPERATIONS ON NUMERIC QUANTITIES REPRESENTED IN A SPECIFIC NOTATION. SUBROUTINES APE PROVIDED TO PERFORM A VARIETY OF ARITHMETIC AND RELATED OPERATIONS.

THE SUBROUTINES ARE DESIGNED TO BE STORED AND EXECUTED IN READ-ONLY-MEMORY(ROM) AND REQUIRE THE FIRST PORTION OF A BANK OF READ-WRITE-MEMORY(RAM) FOR SCRATCHPAD MEMORY. THE SUBROUTINES ARE SEPARATED INTO A NUMBER OF PACKAGES. EACH CONTAINING SUBROUTINES FOR A GROUP OF RELATED OPERATIONS. THE AMOUNT OF MEMORY(ROM AND RAM) REQUIRED FOR INSTALLATION OF THE SYSTEM IS DEPENDENT UPON THE COMBINATION OF PACKAGES TO BE USED. SCRATCHPAD MEMORY IS INITIALIZED BY A UTILITY SUBROUTINE WHICH MUST BE EXECUTED BEFORE OTHER SUBPOUTINES ARE EXECUTED THE FIRST TIME.

IN GENERAL, THE SUBROUTINES HAVE SIMILIAR ENTRY AND EXIT CONDITIONS. UNLESS SPECIFIED DIFFERENTLY IN THE DESCRIPTION OF A SPECIFIC SUBROUTINE, THE SUBROUTINES HAVE THE FOLLOWING CHARACTERISTICS.

SUBROUTINES REQUIRING ONE OPERAND TAKE IT FROM AN INTERNAL FLOATING POINT ACCUMULATOR. SUBROUTINES PEQUIRING TWO OPERANDS TAKE ONE FROM THE ACCUMULATOR AND THE OTHER FROM THE MEMORY LOCATION INDICATED BY THE CONTENTS OF THE H AND L REGISTERS UPON ENTRY. THE NUMERIC RESULT OF EACH OPERATION IS STORED IN THE ACCUMULATOR AND IS RETURNED TO THE CALLER IN THE A, B, C, AND D REGISTERS.

UPON EXIT FROM THE ARITHMETIC SUBROUTINES. THE PROPERTIES OF THE RESULT ARE INDICATED BY THE SETTINGS OF THE CONTROL BITS.

CARRY BIT = 1	THE RESULT EXCEEDS THE CAPACITY OF THE ACCUMULATOR. THE OTHEP CONTROL BITS, THE
	CONTENTS OF THE HARDWARE REGISTERS, AND
	THE CONTENTS OF THE ACCUMULATOR ARE
	MEANINGLESS. THIS SITUATION IS ALSO
	INDICATED BY A NON-ZERO QUANTITY BEING
	STORED IN A FLAG WORD.
CAPRY BIT = 0	THE RESULT IS IN RANGE. THE ZERO AND
	SIGN BITS ARE PROPERLY SET. AND THE A. B.
	C. AND D REGISTERS CONTAIN A REPRESENTATION
	OF THE VALUE IN THE ACCUMULATOR.
ZERO BIT = 1	THE RESULT OF THE OPERATION IS ZERO OR
	A QUANTITY TOO SMALL TO BE REPRESENTED.
7ERO BIT = 0	THE RESULT IS NON-ZERO.
SIGN BIT = 1	THE RESULT IS NEGATIVE.
	THE RESULT IS POSITIVE.
SIGN BIT = $0$	INE REDUCT TO POSITIVE.

DATA ARE REPRESENTED IN A NOTATION WHICH PECORDS EIGHT BITS OF EXPONENT. ONE BIT OF SIGN, AND TWENTY FOUR BITS OF FRACTION. THE LARGEST MAGNITUDE THAT CAN BE REPRESENTED IS APPROXIMATELY 3.6 \* 10 \*\* 38. THE SMALLFST NON-ZERO MAGNITUDE IS APPROXIMATELY 2.7 \* 10 \*\* -39. THE RESOLUTION OF THE NOTATION IS APPROXIMATELY 6.2 \* 10 \*\* -8. I.E., BETTER THAN SEVEN DECIMAL DIGIT PRECISION.

DATA VALUES ARE REPRESENTED IN FOUR CONSERUTIVE MEMORY WORDS WHICH MUST BE IN THE SAME BANK OF MEMORY. THE INTERPRETATION OF THESE WORDS IS SHOWN BELOW. WORD 1 IF NON-ZERO, THIS WORD CONTAINS THE EXPONENT PLUS & BIAS OF 200 OCTAL. THE EXPONENT INDICATES THE POWER OF 2 BY WHICH THE FRACTION IS MULTIPLIED TO OBTAIN THE REPRESENTED VALUE. IF THIS WORD IS ZERO THE REPRESENTED VALUE IS ZERO AND WORDS 2, 3, AND 4 ARE MEANINGLESS. WORD 2. BIT 7 THIS BIT INDICATES THE SIGN OF THE VALUE: 0 IF POSITIVE. 1 IF NEGATIVE. WORD 2. BITS 6-0 THESE BITS PLUS AN ASSUMED 1 IN BIT 7 ARE THE MOST SIGNIFICANT BITS OF THE FRACTION. THE FRACTION IS STORED IN ABSOLUTE FORM (UNSIGNED) WITH THE RADIX POINT POSITIONED TO THE LEFT OF BIT 7. THE VALUE OF THE FRACTION IS THUS LESS THAN 1.0 AND EQUAL TO OR GREATER THAN 0.5. WORD 3 THIS WOPD CONTAINS THE SECOND MOST SIGNIFICANT EIGHT BITS OF THE FRACTION. WORD 4 THIS WORD CONTAINS THE LEAST SIGNIFICANT EIGHT BITS OF THE FRACTION. EXAMPLES OF DATA NOTATION.

VALUE	WORD1	WORD2	WORD3	WORD4				
0.0	000	XXX	XXX	XXX	X	=	DONT	CA
+1.0	201	000	000	000				
-1.0	201	200	000	000				
+0.1	175	114	314	314				
-100.1	207	310	063	063				

FLOATING POINT ACCUMULATOR.

THE FLOATING POINT ACCUMULATOR CONSISTS OF 5 SCRATCHPAD WORDS CONTAINING RESPECTIVELY THE ACCUMULATOR EXPONENT, THE ACCUMULATOR SIGN, AND THREE WORDS OF ACCUMULATOR FRACTION. THE EXPONENT IS RECORDED WITH A BIAS OF 200 OCTAL. AN EXPONENT WORD OF ZERO INDICATES THAT THE VALUE IN THE ACCUMULATOR IS 7ERO AND THE REMAINING WORDS OF THE ACCUMULATOR ARE MEANINGLESS. THE SIGN WORD HOLDS 000 IF THE ACCUMULATOR IS NEGATIVE, 200 OCTAL IF POSITIVE. THE FRACTION IS RECORDED AS A NORMALIZED POSITIVE VALUE WITH THE RADIX POINT TO THE LEFT OF THE MOST SIGNIFICANT BIT OF THE FIRST FRACTION WORD.

RE

#### OVERFLOW FLAG.

THE OVERFLOW FLAG WORD IS PROVIDED AS A CONVENIENCE TO THE USER OF THE FLOATING POINT SYSTEM. THE WORD IS INITIALLY SET TO ZERO AND MAY BE RESET TO ZERO BY THE USER AT ANY TIME. WHEN ANY OF THE SYSTEM SUBPOUTINES DETECT AN OVERFLOW CONDITION THE OVERFLOW FLAG IS SET NON-ZERO. THUS THE USER MAY CLFAR THE FLAG, PERFORM A SEQUENCE OF FLOATING POINT OPERATIONS, AND CHECK THE FLAG TO DETERMINE IF AN OVERFLOW OCCURRED ANYWHERE IN THE SEQUENCE.

### 8008 BINARY FLOATING POINT SYSTEM

THE 8008 BINARY FLOATING POINT SYSTEM CONSISTS OF A SET OF SUBPOUTINES DESIGNED TO PERFORM ARITHMETIC OPFRATIONS ON NUMERIC QUANTITIES REPRESENTED IN MEMORY.

FACH NUMERIC QUANTITY OCCUPIES FOUR CONSECUTIVE WORDS (32 BITS) OF MEMORY. THE LARGEST MAGNITUDE THAT CAN HE REPRESENTED IS APPROXIMATELY 3.6 TIMES TEN TO THE 39TH POWER. THE SMALLEST NON-ZERO MAGNITUDE THAT CAN BE REPRESENTED IS APPROXIMATELY 2.7 TIMES TEN TO THE MINUS 39TH POWER. EACH NUMERIC QUANTITY IS REPRESENTED WITH A PRECISION OF ONE PART IN APPROXIMATELY 16,000,000.

THE SOFTWAPE CONSTITUTING THE FLOATING POINT SYSTEM IS DIVIDED INTO TWO SECTIONS. EACH OF WHICH OCCUPIES 3 BANKS OF ROM OR RAM. SECTION 1 IS INDEPENDENT OF OTHER SOFTWARE. SECTION 2 IS OPEPABLE ONLY WHEN SECTION 1 IS AVAILABLE IN MEMORY. IN ADDITION TO MEMORY REQUIRED FOR PROGRAM. 63 WORDS OF RAM ARE USED AS SCPATCHPAD.

SOFTWARE SECTION 1 CONTAINS THE FOLLOWING SUBROUTINES:

- LOD LOAD SPECIFIED DATA INTO THE FLOATING POINT ACCUMULATOR.
- ADD ADD SPECIFIED DATA TO THE FLOATING POINT ACCUMULATOP.
- SUB SUBTRACT SPECIFIED DATA FROM THE FLOATING POINT ACCUMULATOR.
- MUL MULTIPLY SPECIFIED DATA TIMES THE FLOATING POINT ACCUMULATOR. 3
- DIV DIVIDE SPECIFIED DATA INTO THE FLOATING POINT ACCUMULATOR.
- TST SET CONTROL BITS TO INDICATE ATTRIBUTES OF THE FLOATING POINT ACCUMULATOR.
- CHS CHANGE THE SIGN OF THE FLOATING POINT ACCUMULATOR.
- ABS SET THE SIGN OF THE FLOATING POINT ACCUMULATOR POSITIVE.
- STR STORE IN SPECIFIED MEMORY THE VALUE IN THE PEGISTERS AS RETURNED BY OTHER SUBROUTINES.
- INIT MOVE CODE FROM ROM TO RAM IN PREPARATION FOR EXECUTION OF THE MUL AND DIV SUBROUTINES.

SOFTWAPE SECTION 2 CONTAINS SUBROUTINES WHICH ARE USED TO CONVERT DATA BETWEEN THE BINARY FLOATING POINT FORMAT AND A DECIMAL FORMAT SUITABLE FOR ENTRY OR DISPLAY ON INPUT/OUTPUT EQUIPMENT. THE DECIMAL FORMAT IS STORED IN MEMORY AS A SEPIES OF CHARACTERS. RELATIVELY SIMPLE INPUT/OUTPUT POUTINES MAY BE USED TO INTERFACE THE MEMORY-RESIDENT CHARACTER STRINGS WITH ANY TYPE OF PHYSICAL I/O DEVICE.

THE CHARACTER STRINGS CONSIST OF BCD REPRESENTATIONS OF DECIMAL DIGITS AND ARBITRARY REPRESENTATIONS OF +, -, .. AN EXPONENTIAL SIGN(LETTER E), AND SPACE. CHARACTER STRINGS MAY NOT CROSS MEMORY BANK BOUNDARIES. AN INPUT STRING IS THEREFORE LIMITED TO 256 CHARACTERS. AN OUTPUT STRING CONSISTS OF 13 CHARACTERS.

THE OUT SUBPOUTINE GENERATES CHARACTER STRINGS IN 2 FORMATS: THE CHOICE OF FORMAT DEPENDS ON THE MAGNITUDE OF THE VALUE REPRESENTED. MAGNITUDES BETWEEN .1000000 AND 9999999. ARE REPRESENTED BY A SPACE OR MINUS SIGN, SEVEN DECIMAL DIGITS AND AN APPROPRIATELY POSITIONED DECIMAL POINT. AND FOUR SPACES. MAGNITUDES OUTSIDE THE RANGE ARE REPRESENTED BY A SPACE OR MINUS SIGN, A VALUE BETWEEN 1.000000 AND 9.999999. AN EXPONENTIAL SIGN. AND A SIGNED TWO-DIGIT POWER OF TEN.

THE INP SUBROUTINE CONVERTS CHARACTER STRINGS IN EITHER OF THE ABOVE FORMATS, OR A MODIFIED VERSION OF THEM. THE LEADING SIGN MAY BE INCLUDED OR OMITTED. ANY NUMBER OF DIGITS MAY BE USED TO INDICATE THE VALUE. WITH OR WITHOUT AN INCLUDED DECIMAL POINT. IF A POWER-OF-TEN MULTIPLIER IS INDICATED IT MAY BE SIGNED OR UNSIGNED AND MAY CONTAIN ONF OR TWO DIGITS. AN INPUT STRING IS TERMINATED BY THE FIRST CHARACTER WHICH DEPARTS FROM THE FORMAT.

THE FOLLOWING ARE EXAMPLES OF INPUT AND CORRESPONDING OUTPUT CHARACTER STRINGS.

3.141593	3.141593
00000000000	-1.000000F-13
+1.6E5	160000.0
123456789	1.234568E+08
54321E-10	5.432100E-06
-2718281828F-9	-2.718282

## 8008 BINARY FLOATING POINT SYSTEM

FORMAT CONVERSION PACKAGE

THE FORMAT CONVERSION PACKAGE OF THE BOOB BINARY FLOATING POINT SYSTEM CONTAINS SUBROUTINES FOR THE CONVERSION OF DATA BETWEEN THE FLOATING POINT SYSTEM NOTATION AND TWO OTHER FORMATS. THE NON-FLUATING-POINT FORMATS ARE FOUR WORD FIXED POINT FORMAT AND VARIABLE LENGTH CHARACTER STRING FORMAT.

THE FORMAT CONVERSION PACKAGE IS CONTAINED IN 512 CONSECUTIVE WORDS OF MEMORY (2 BANKS OF ROM) AND REQUIRES FOR ITS EXECUTION THAT THE ARITHMETIC AND UTILITY PACKAGE BE AVAILABLE IN MEMORY. THE COMBINATION OF THIS PACKAGE AND THE ARITHMETIC AND UTILITY PACKAGE USES THE FIRST 64 WORDS OF A BANK OF RAM AS SCRATCHPAD MEMORY.

THE FIXED POINT FORMAT DATA PROCESSED BY THIS PACKAGE CONSIST OF 32 BIT BINARY NUMBERS OCCUPYING FOUR WORDS. TWOS COMPLEMENT NOTATION IS USED TO REPRESENT NEGATIVE VALUES.

THE POSITION OF THE BINARY POINT RELATIVE TO THE BITS REPRESENTING THE VALUE IS DENOTED BY A BINARY SCALING FACTOR. THE RINARY SCALING FACTOD IS NOT NORMALLY RECORDED IN THE COMPUTER, BUT WHEN A FORMAT CONVERSION SUBROUTINE IS CALLED THE BINARY SCALING FACTOP MUST BE SPECIFIED (IN THE E REGISTER). A BINARY SCALING FACTOR OF ZERO INDICATES THE BINARY POINT IS IMMEDIATELY TO THE LEFT OF THE MOST SIGNIFICANT OF THE 32 BITS REPRESENTING THE VALUE. A BINARY SCALING FACTOR OF 32 INDICATES THE BINARY POINT IS IMMEDIATELY TO THE RIGHT OF THE LEAST SIGNIFICANT BIT. THE PERMISSIBLE RANGE OF THE BINARY SCALING FACTOR IS -128(200 OCTAL) TO +127(177 OCTAL).

THE CHARACTER STRING FORMAT DATA PROCESSED BY THIS PACKAGE CONSIST OF BINARY REPRESENTATIONS OF CHARACTERS OCCUPYING CONSECUTIVE WORDS OF MEMORY. A CHARACTER STRING MAY NOT CROSS A MEMORY BANK BOUNDARY. THE CHARACTERS WHICH MAY BE INCLUDED IN A CHARACTER STRING. AND THE CORRESPONDING OCTAL REPRESENTATIONS ARE LISTED BELOW.

	DECIMAL DIGITS	0008-0118	BCD DIGITS		
•	SPACE	3608			
1	•	3738 PLUS			
	-	3758 MINUS			
1	•	376B DECIM	AL POINT		
	EXPONENTIAL SIGN	D258 LETTE	RE		
THESE	OCTAL REPRESENTAT	IONS CAN B	E CONVERTED	TO	THE
CORRESP	ONDING ASCII CHAP	ACTERS BY	ADDING 060B	TO	EACH)

THE OUT SUPROUTINE GENERATES CHARACTER STRINGS IN TWO FORMATS. EACH CONSISTING OF 13 CHARACTERS. THE FORMAT USED IN A SPECIFIC CASE IS DEPENDENT UPON THE MAGNITUDE OF THE VALUE REPRESENTED.

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### SIGNIFICANCE INDEX

THE FLOATING POINT ADD AND SUBTRACT SUBROUTINES RETURN A SIGNIFICANCE INDEX TO THE USER WHEN THE RESULT OF THE OPERATION IS NOT ZERO. THIS INDEX GIVES AN INDICATION OF THE CHANGE IN THE VALUE OF THE ACCUMULATOR EXPONENT AS A RESULT OF THE ARITHMETIC OPERATION PERFORMED. IT IS USED PRIMARILY FOR COMPARISON OF TWO VALUES WHICH ARE EXPECTED TO BE EQUAL. BUT WHICH MAY DIFFER BY A SMALL AMOUNT DUF TO MEASUREMENT OR ROUND-OFF ERRORS. AS AN EXAMPLE, A SIGNIFICANCE INDEX OF 354 OCTAL (-20 DECIMAL) INDICATES THAT THE RESULT OF THE OPERATION IS SMALLER THAN THE OPERANDS BY A FACTOR OF APPROXIMATELY ONE MILLION (2 \*\* 20). THE FLOATING POINT TEST. COMPLEMENT AND ARSOLUTE SUBROUTINES RETURN THE SIGNIFICANCE INDEX FROM AN IMMEDIATELY PRECEDING ADD OR SUBTRACT OPERATION. 

#### HEXADECIMAL NOTATION [4]

Hexadecimal Notation is a convenient way of representing all sixteen combinations of four bits of information with a single character. The most popular character set for displaying Hexadecimal data are the characters 0 thru 9 to represent the binary combinations 0 thru 9 and A B C D E and F to represent the binary combinations 10 thru 15.

Hexadecimal	Binary Bits	Decimal
Characters	8 4 2 1	Characters
0 1 2 3 4 5 6 7 8 9 A B C D E F	$\begin{array}{cccccccccccccccccccccccccccccccccccc$	0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

As an extension of this technique, all 256 combinations of 8 bits can be represented by two hexadecimal characters as shown in the following examples.

Hexadecimal	Binary	Decimal
Characters	Bits	Characters
00	0000 0000	0
01	0000 0001	1
3E	0011 1110	52
42	0100 0010	66
E1	1110 0001	225
FF	1111 1111	255

Going further, all 4096 combinations of 12 bits can be represented by three Hexadecimal characters. This technique can be extended indefinitely, adding a Hexadecimal character for each four bits of information.



### LIST OF REFERENCES

- Russell, R. W., <u>A Design Study for a Center Plate</u> <u>Mount for a Wind-tunnel Model</u>, M. S. Thesis, Naval Postgraduate School, Monterey, California, 1977.
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- Intel Corporation, <u>MCS User's Library</u>, p. 8-7, Intel, 1972.
- Biewer, M, <u>The Designers Guide To Programmed Logic</u>, p. 1-1 to 6-0, Prolog Corporation, 1975.

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